SCHEMATIC DIAGRAM

MODEL : 42DPC85

WARNING : BEFORE SERVICING THIS CHASSIS, READ THE "SERVICE SAFETY PRECAUTIONS" ON THE MANUAL FOR THIS MODEL.

CAUTION : The international hazard symbols "⚠" in the schematic diagram and the parts list designate components which have special characteristics important for safety and should be replaced only with types identical to those in the original circuit or specified in the parts list. The mounting position of replacements is to be identical with originals. Before replacing any of these components, read carefully the SERVICE SAFETY PRECAUTIONS on the MANUAL for this model. Do not degrade the safety of the receiver through improper servicing.

NOTE:

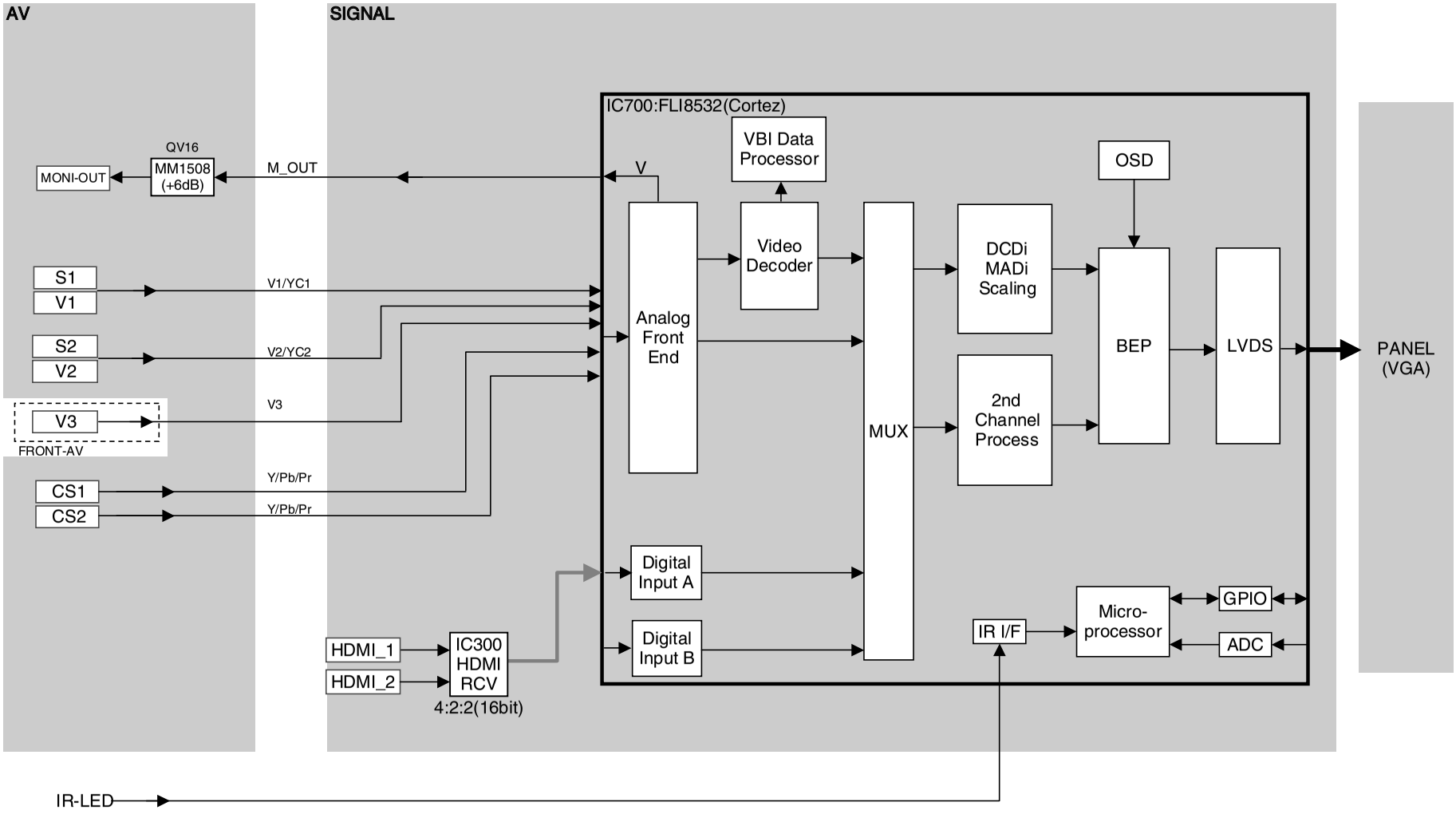
1. RESISTOR Resistance is shown in ohm [K = 1.000, M = 1.000.000]. All resistors are 1/6W and 5% tolerance carbon resistor, unless otherwise noted as the following marks.
   1/2R = Metal or Metal oxide of 1/2 watt 1/2S = Carbon compsistion of 1/2 watt
   1RF = Fuse resistor of 1 watt 10W = Cement of 10 watt
   K = ±10% G = ±2% F = ±1%
2. CAPACITOR Unless otherwise noted in schematic, all capacitor values less than 1 are expressed in μF, and the values more than 1 in pF.
   All capacitors are ceramic 50V, unless otherwise noted as the following marks.
    Electolytic capacitor  Mylar capacitor
3. The parts indicated with " ⚠ " have special characteristics, and should be replaced with identical parts only.
4. Voltages read with DIGITAL MULTI-METER from point indicated to chassing ground, using a color bar signal with all controls at normal, line voltage 220 volts.
5. Waveforms are taken receiving color bar signal with enough sensitivity.
6. Voltage reading shown are nominal values and may vary ±20% except H.V.

■ **SCHEMATIC DIAGRAM STRUCTURE:**

AV/AOUT
- CONNECTOR [SHEET-1/4] 1/47
- AV-TERMINAL [SHEET-2/4] 2/47
- A-OUT [SHEET-3/4] 3/47
- EXPANDER FOR PDP [SHEET-4/4] 4/47

FRONT-AV 5/47
IR LED 6/47
KEY 7/47
LOWB 8/47
SIGNAL
- ANALOG IN [SHEET-200] 9/47
- D IN A [SHEET-300] 10/47
- D IN B [SHEET-301] 11/47
- HDMI (Link, I2C) [SHEET-302] 12/47
- HDMI Rx#2 (Video/Audio) [SHEET-303] 13/47
- HDMI Rx#3 (PWR,Audio) [SHEET-304] 14/47
- HDMI AUDIO PLL [SHEET-305] 15/47
- HDMI AUDIO DAC [SHEET-306] 16/47
- HDMI AUDIO OUT [SHEET-307] 17/47
- HDMI CONTROLLER [SHEET-308] 18/47
- EEPROM1 [SHEET-309] 19/47
- EEPROM2 [SHEET-310] 20/47
- MICRO I/O [SHEET-400] 21/47
- OCM MEMORY I/F [SHEET-401] 22/47
- FLASH MEMORY [SHEET-402] 23/47
- SYNC SEPA [SHEET-403] 24/47
- E2P OTHER [SHEET-404] 25/47
- STD-BY MICOR [SHEET-405] 26/47
- CORTEZ REG 1 [SHEET-406] 27/47
- CORTEZ REG 2 [SHEET-407] 28/47
- CORTEZ REG 3 [SHEET-408] 29/47
- BOOT CONFIG [SHEET-409] 30/47
- SERVICE CONNECTOR [SHEET-410] 31/47
- I2C Switch [SHEET-411] 32/47
- STBY MICRO for DVD internal [SHEET-412] 33/47
- DVD 10pin connector [SHEET-413] 34/47
- I2C Level shift [SHEET-414] 35/47
- AUDIO [SHEET-600] 36/47
- AUDIO MSP [SHEET-601] 37/47
- CORTEZ 1 [SHEET-700] 38/47
- CORTEZ 2 [SHEET-701] 39/47
- DDR I/F [SHEET-702] 40/47
- DDR SDRAM [SHEET-703] 41/47
- DDR TERMINATION [SHEET-704] 42/47
- DCDC CONV. [SHEET-802] 43/47
- LVDS OUT [SHEET-900] 44/47
- LVDS OUT(SHARP LCD) [SHEET-901] 45/47
- Power Connector and Dimming [SHEET-902] 46/47
- LVDS Power and Others [SHEET-903] 47/47

CIRCUIT BLOCK DIAGRAM



OTHER PARTS

- U121 PW8 PC****-1
- NV01 SOLDEREX3625
- NV02 SOLDER9025SE
- NV03 SOLDER9025SE
- NV04 THINNER4520
- NV05 FLUX CF330VH
- NV06 FLUX CF330VH
- NV07 SOLDEREX9025
- NV08 SOLDER9025SE
- NV09 SOLDER9025SE
- NV10 THINNER4520
- NV11 FLUX CF330VH

TO SIGNAL BOARD

50P-FFC

PJ22A

9637S-50ATLIFFC/FPC 0.5mm50P SOCKET

50P-FFC

PJ60A

9637S-50ATLIFFC/FPC 0.5mm50P SOCKET

PJ21A

PLUG_2mm_3P,B3B-PH-K-S

3P

7P

FRONT-AV(E3)

PV25A

B7B-PH-K-S

5P

FRONT-AV

P602A

B5B-PH-K-S

12P

P803B

W-B 12P_2.5mm_1.5A_Beige

TO LOWB BOARD

6P

P804B

6P_2.5mm_1.5A_Beige

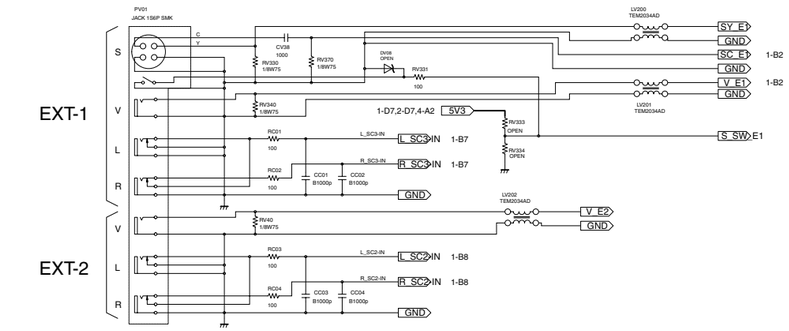
RV67 OPEN

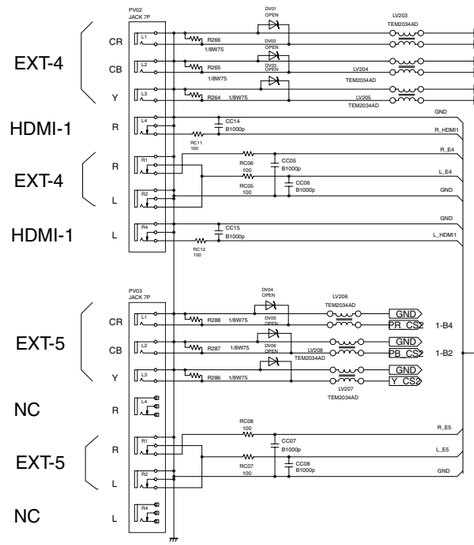
RV68 OPEN

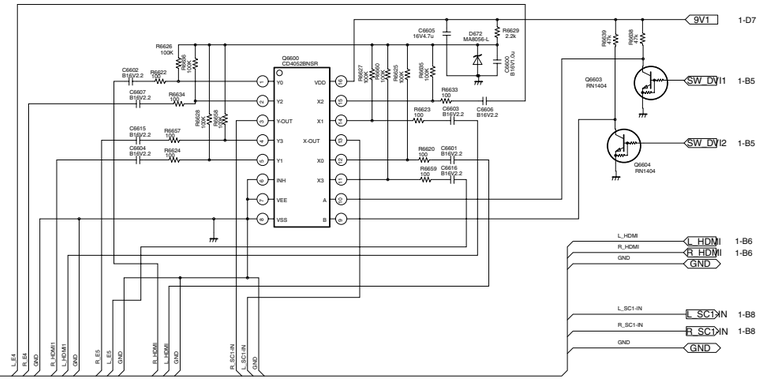
RV69 OPEN

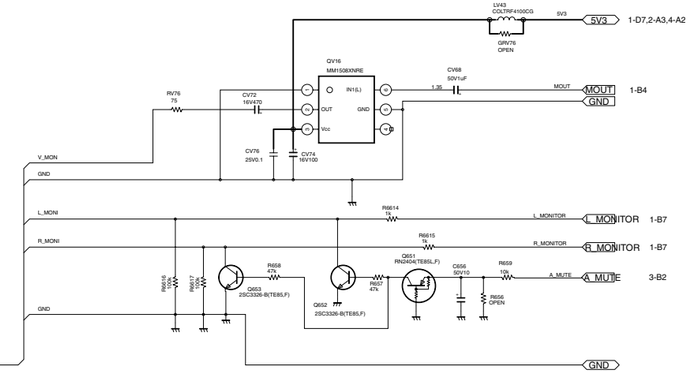
RV60 OPEN

42DPC85
AV/AOUT
CONNECTOR
[SHEET-1/4]





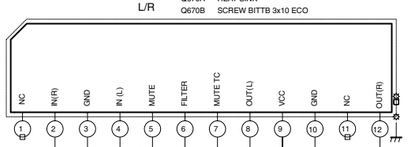




42DPC85
AV/AOUT
AV-TERMINAL
[SHEET-2/4]

Q670 TA8246AHQ
Q670A HEAT SINK
Q670B SCREW BITTB 3x10 ECO

L/R



1-B7 R APRO R_APRO
GND
C660 50V4.7
R674 OPEN
C666 OPEN
R670 4.7k
R672 1.8k
C662 B680p
R676 OPEN M
C664 OPEN

1-B7 L APRO L_APRO
GND
C661 50V4.7
R671 4.7k
R675 OPEN
C667 OPEN
R673 1.8k
C663 B680p
R677 OPEN M
C665 OPEN

1-B6 MUTE
D676 KDS160-RTK/P
C669 B1000p
R664 100K

Q672 2SC3326-B(TE85,F)
R681 47K
C691 B16V0.22
R680 47K
C675 OPEN
C692 B16V0.22
Q671 2SC3326-B(TE85,F)

C670 50V10
C671 50V10
R6604 1k
C673 16V47u
C674 B16V1.0u
C678 B50V0.1
R678 1/8W 2.2
D670 KDS160-RTK/P
D671 KDS160-RTK/P
C680 35V1000
C684 B50V0.01
D674 KDS160-RTK/P
D675 KDS160-RTK/P
D607 MA8330-M
C679 B50V0.1
R679 1/8W 2.2

26V → 26V 1-E7
GND_AUDIO → GND_AUDIO
VCC_GND
GND_AUDIO → GND_AUDIO
C681 35V2200
L_OUT → L_OUT 1-C7
C682 35V2200
R_OUT → R_OUT 1-C7
GND_AUDIO → GND_AUDIO

D680 KETSU

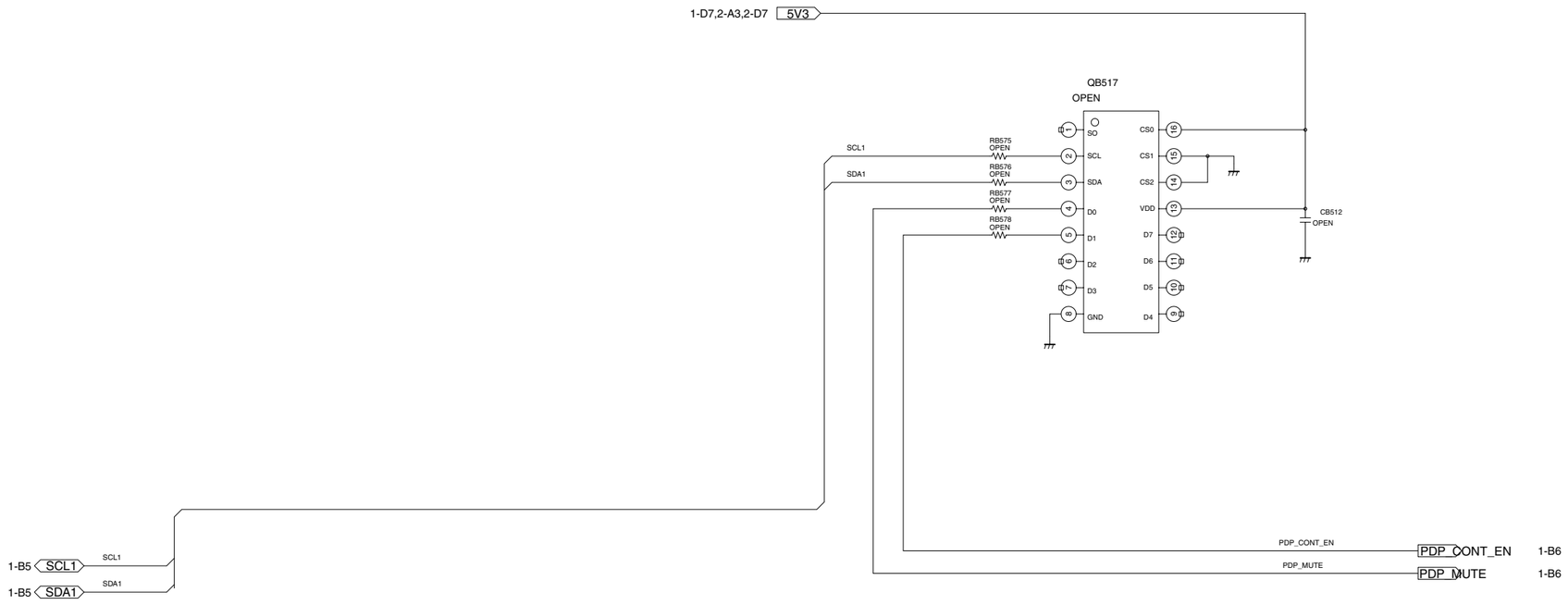
1-D7 5V1 5V1
R685 2.2k
D678 KDS160-RTK/P
C690 6.3V1000
Q673 2SA1162-Y
R686 1k
R684 100k
R683 10k
C677 B16V1.0u
C685 10V1u
R692 22k
R691 10k
Q660 2SC4081-Q
Q661 2SC4081-Q

1-D7 ACDETECT ACDETECT
R690 47k

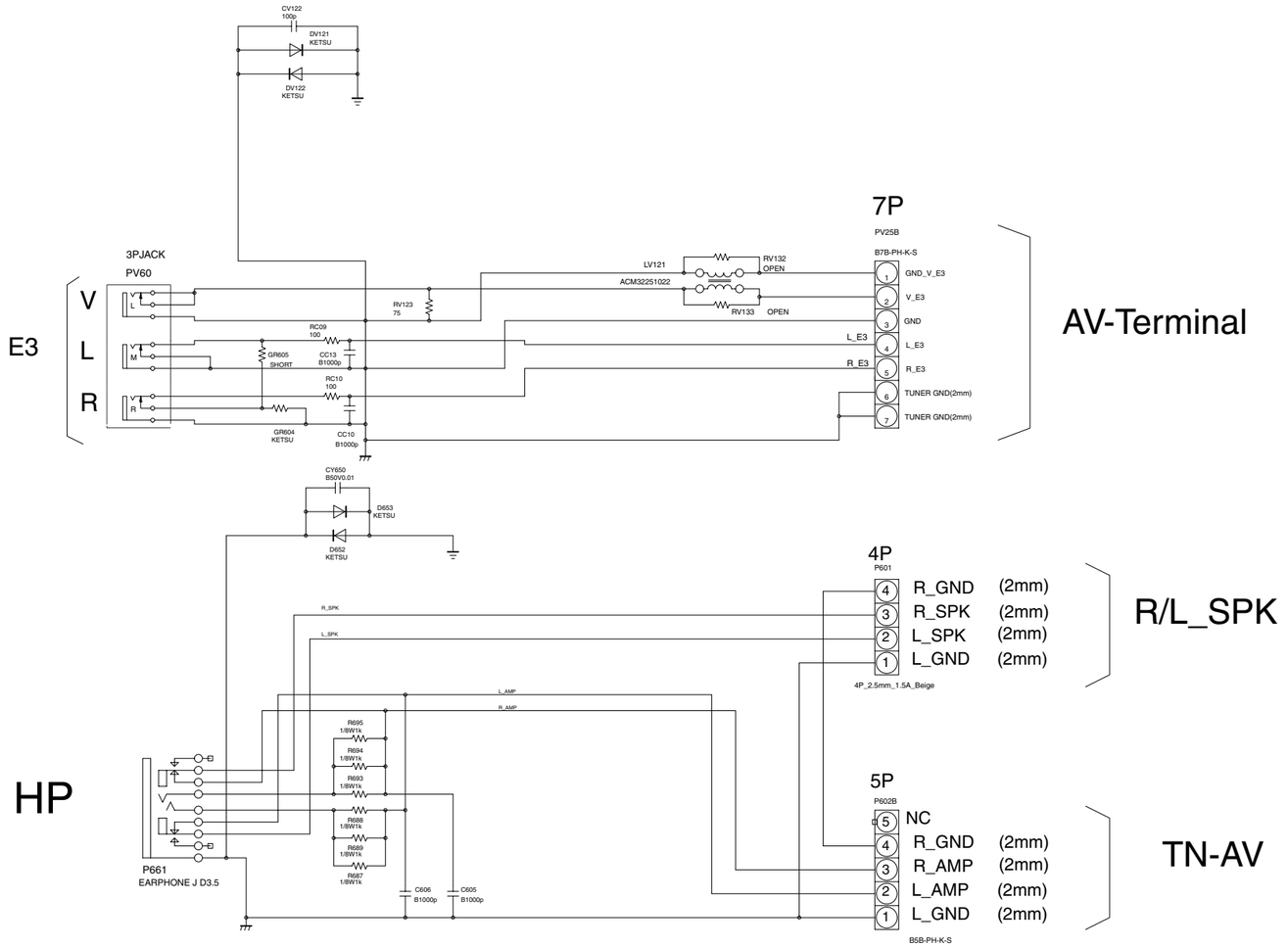
R6605 10k
Q665 RN2404(TE85L,F)
1-B6 EXT MUTE EXT_MUTE
R6603 22k
Q664 2SC4081-Q
R6606 100K

A_MUTE → A_MUTE 2-E7

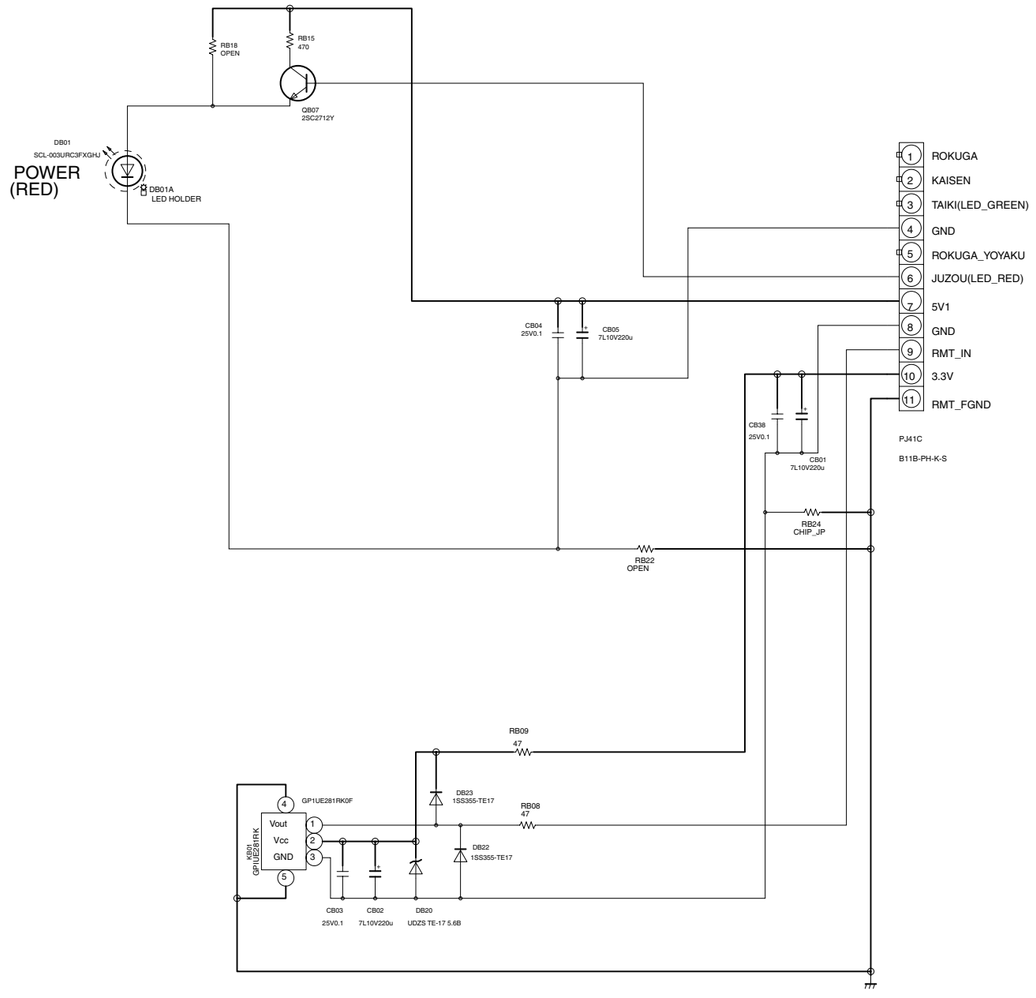
42DPC85
AV/AOUT
A-OUT
[SHEET-3/4]



42DPC85
AV/AOUT
EXPANDER FOR PDP
[SHEET-4/4]



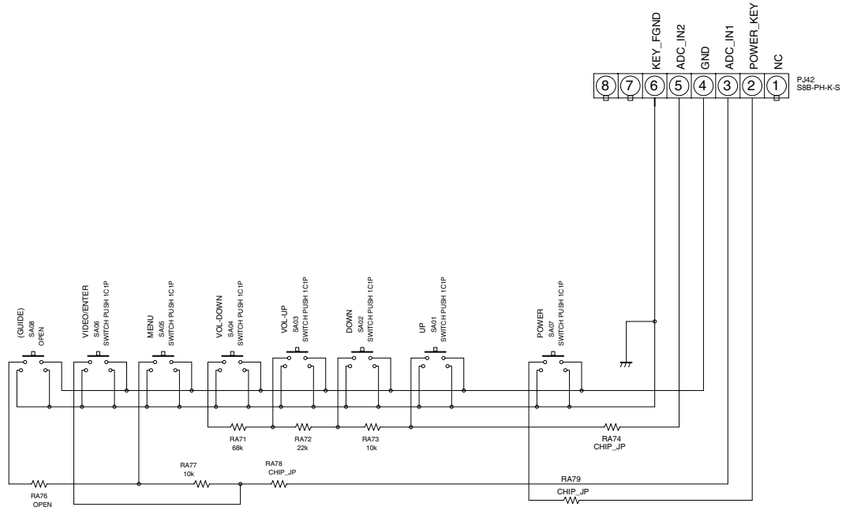
42DPC85
FRONT-AV



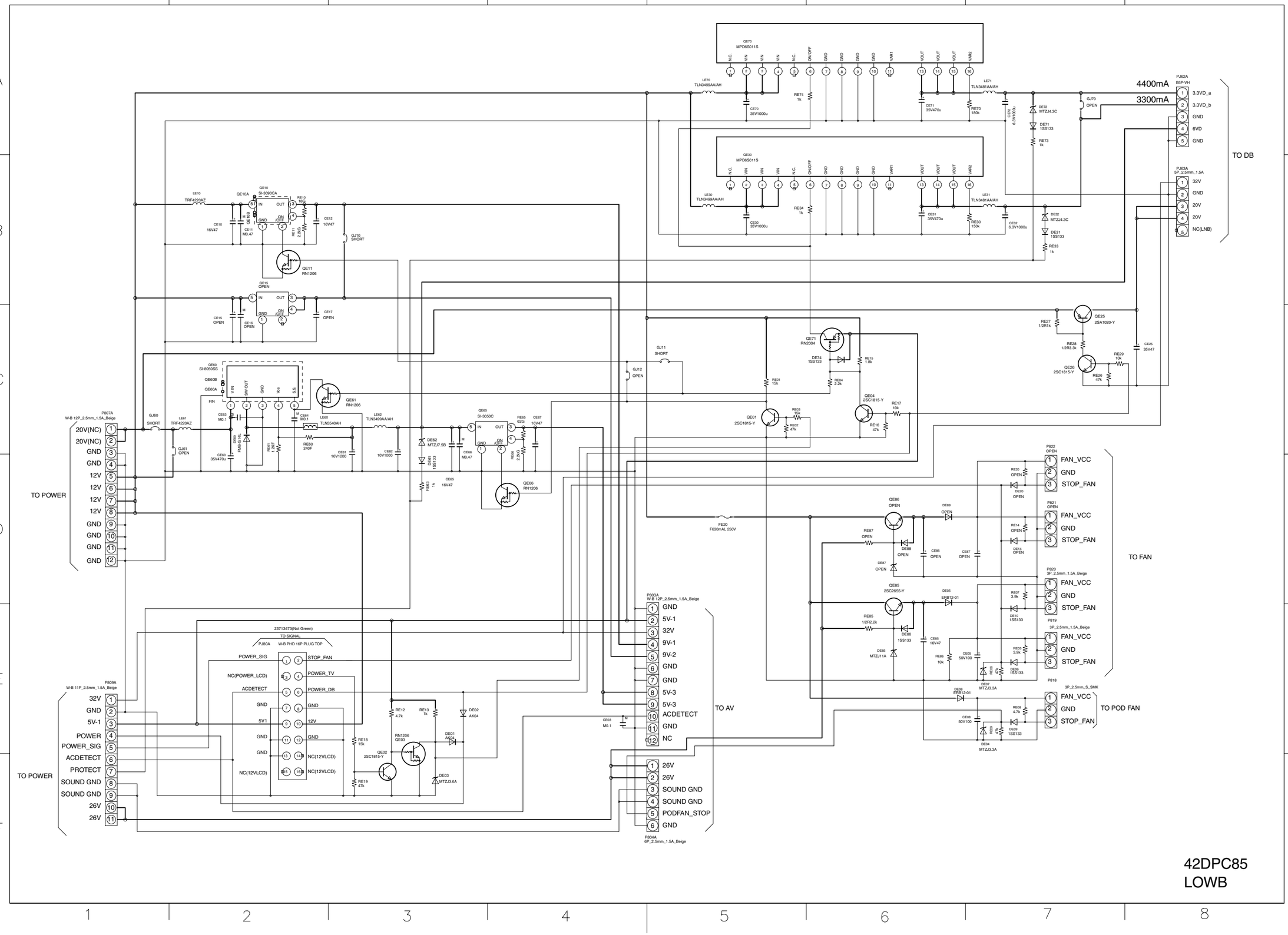
42DPC85
IR LED

OTHER PARTS

U123
PWB PD****-3



42DPC85
KEY


4400mA
3300mA
TO DB
TO FAN
TO POD FAN
TO AV
TO POWER
TO POWER
TO SIGNAL
23713473(Not Green)
P.J80A W-B PHD 16P PLUG TOP
POWER_SIG
NC(POWER_LCD)
ACDETECT
GND
5V1
GND
GND
NC(12VLCD)
STOP_FAN
POWER_TV
POWER_DB
GND
12V
GND
NC(12VLCD)
NC(12VLCD)
42DPC85
LOWB

(8/47)

1:SCL1	18:GND	35:B_PC
2:SDA1	19:Pb_DVD/SC_E3	36:GND
3:POD_STOP_FAN	20:GND	37:Y_CS
4:AV/S_DET1	21:G_PC	38:GND
5:MONI_SW	22:GND	39:NC/V_E3
6:AFT1	23:V_TV	40:GND
7:NC	24:RET	41:SC_E1
8:EPG_SW	25:GND	42:RET
9:V_MONI	26:V_E2	43:GND
10:GND	27:GND	44:NC/SC_E2
11:V_EPG	28:R_PC	45:GND
12:NC	29:GND	46:Pb_CS
13:GND	30:Pr_CS	47:GND
14:V_DGT	31:GND	48:V_E1
15:RET	32:SY_E1	49:GND
16:GND	33:RET	50:Y_DVD/SY_E3
17:Pr_DVD/SY_E2	34:GND	

G36220157026 — PJ22 [CN50]

301:2B V_BS
301:2B V_BS_GND
301:2B Y_BS
301:2B Y_BS_GND
308:2B:400:1E:414:4E:601:3A SDA1
308:2B:400:1E:414:4E:601:3A SCL1
401:6C GP1
1E GP2
1D GP3
1E GP4
2D GP5
3E GP6
600:3B GP9
600:3B GP10
1C AV/S_DET1 GP5
1C GP3 AFT1
1C GP4 MONI_SW
1C GP2
HD_Dsub
VD_Dsub
+3.3V_I/O
V_TV 403:2C
NOT MOUNT R2034
R2009 10K(J)
NOT MOUNT R2037 401:6B D4_SW D221
R2036 [OZ] 33(J) AV/S_DET1 300:5F D222 [OZ]
R2058 33(J) R2048 33(J) D4_L1 400:1C D219 [OZ]
R2032 33(J) R2042 33(J) MONI_SW 401:6C D218 [ZD]
R2045 NOT MOUNT R2049 NOT MOUNT D4_L3 400:1C D217 NOT MOUNT
R2031 33(J) R2041 33(J) S1/S2_DET 405:4C:412:1B D223 [ZD]
R2039 NOT MOUNT
R2059 NOT MOUNT 401:6E AV/S_DET2 D220 [ZD]
R2040 NOT MOUNT
R2047 NOT MOUNT
R2029 NOT MOUNT
R2046 NOT MOUNT
W204
W203 NOT MOUNT ON
W219 NOT MOUNT
W201
301:2B V_SIG V_SIG_GND 301:2B:403:1E
R2010 33(J) R2011 33(J)
+3.3V_I/O V_SYNC_SEPA 403:1E
MPZ2012S221AT FL201
C2020 100nF(K)
Q2001 2SC4116-Y(TE85L,F)
R2050 10K(J) R2051 10K(J)
R2012 33(J) R2013 33(J)
R2061 100(J) R2062 560(J)
R2014 33(J)
AV/S_DET2
AV/S_DET3
R2008 33(J) R2057 33(J)

UDZSTE-176.2B D208 [OZ] — NOT MOUNT D207 — UDZSTE-176.2B D206 [OZ] — UDZSTE-176.2B D205 [OZ] — NOT MOUNT D204 — UDZSTE-176.2B D203 [OZ] — UDZSTE-176.2B D202 [OZ] — UDZSTE-176.2B D201 [OZ] — GND

R2015 C2008 22(J) 100nF(K)
R2000 C2000 22(J) 100nF(K)
R2016 C2009 22(J) 100nF(K)
R2001 C2001 22(J) 100nF(K)
R2017 C2010 56(J) 100nF(K)
R2018 C2011 22(J) 100nF(K)
R2002 C2002 22(J) 100nF(K)
R2019 C2012 22(J) 100nF(K)
R2003 C2003 22(J) 100nF(K)
R2020 C2013 56(J) 100nF(K)
R2021 C2014 22(J) 100nF(K)
R2004 C2004 22(J) 100nF(K)
R2022 C2015 22(J) 100nF(K)
R2005 C2005 22(J) 100nF(K)
R2023 C2016 56(J) 100nF(K)
R2024 C2017 22(J) 100nF(K)
R2006 C2006 22(J) 100nF(K)
R2025 C2018 22(J) 100nF(K)
R2007 C2007 22(J) 100nF(K)
R2026 C2019 56(J) 100nF(K)

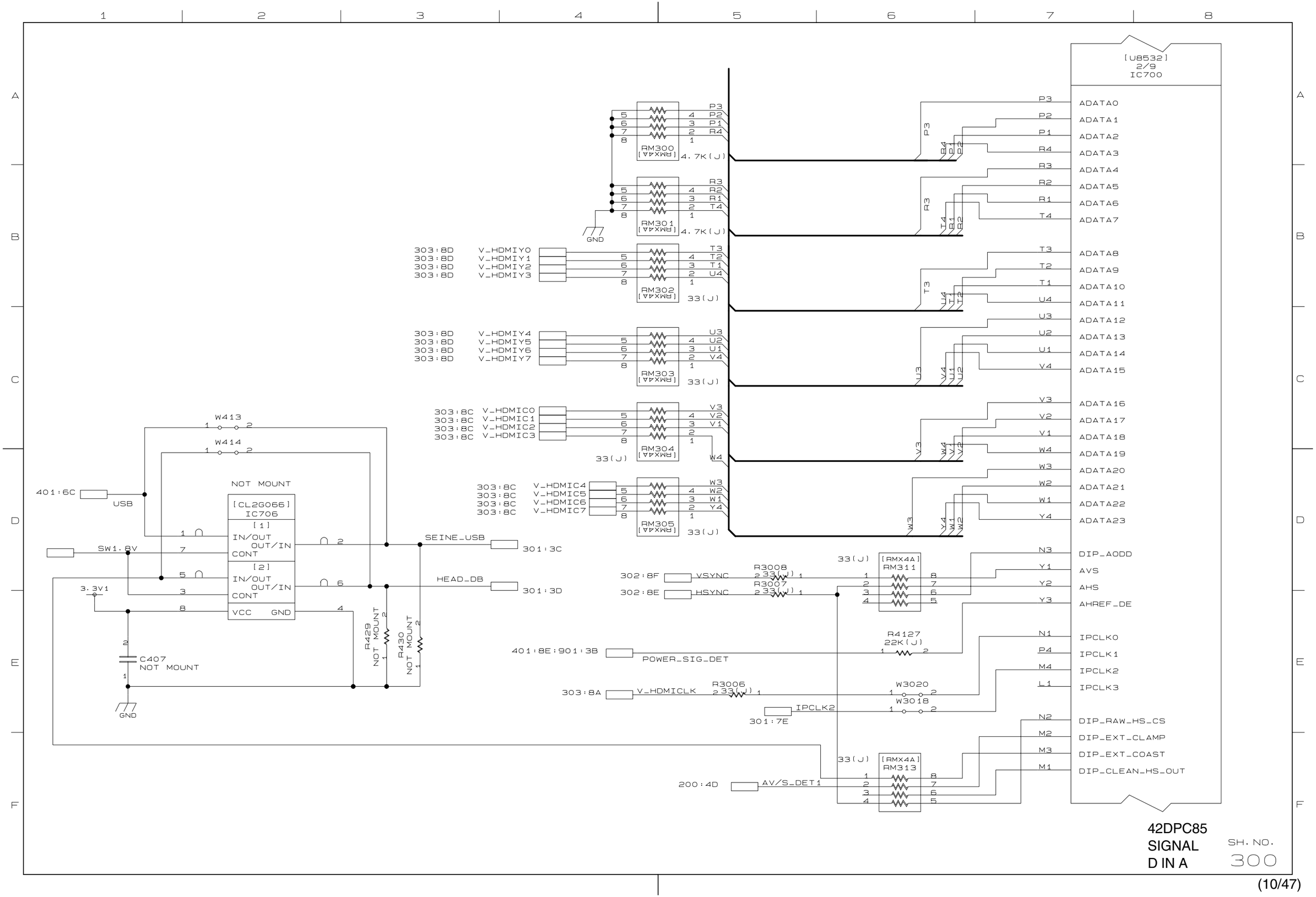
UDZSTE-176.2B [ZD] D216 — D215 — D214 — D213 — D212 — D211 — NOT MOUNT D210 — D209

[U8532] 1/9 IC700

AB1	A1P
AF1	A2P
AF4	A3P
AF7	A4P
AD1	AN
AC2	B1P
AE2	B2P
AE5	B3P
AE8	B4P
AF3	BN
AC1	C1P
AF2	C2P
AF5	C3P
AF8	C4P
AF6	CN
AB2	SV1P
AE1	SV2P
AE4	SV3P
AE7	SV4P
AF9	SVN
AA1	NC
AC7	VOUT2
AF12	AIP_RAW_HS_CS
AE12	AIP_RAW_VS
AD13	EXT_OSD_VS
AC13	EXT_OSD_HS
AF13	EXT_OSD_CLK

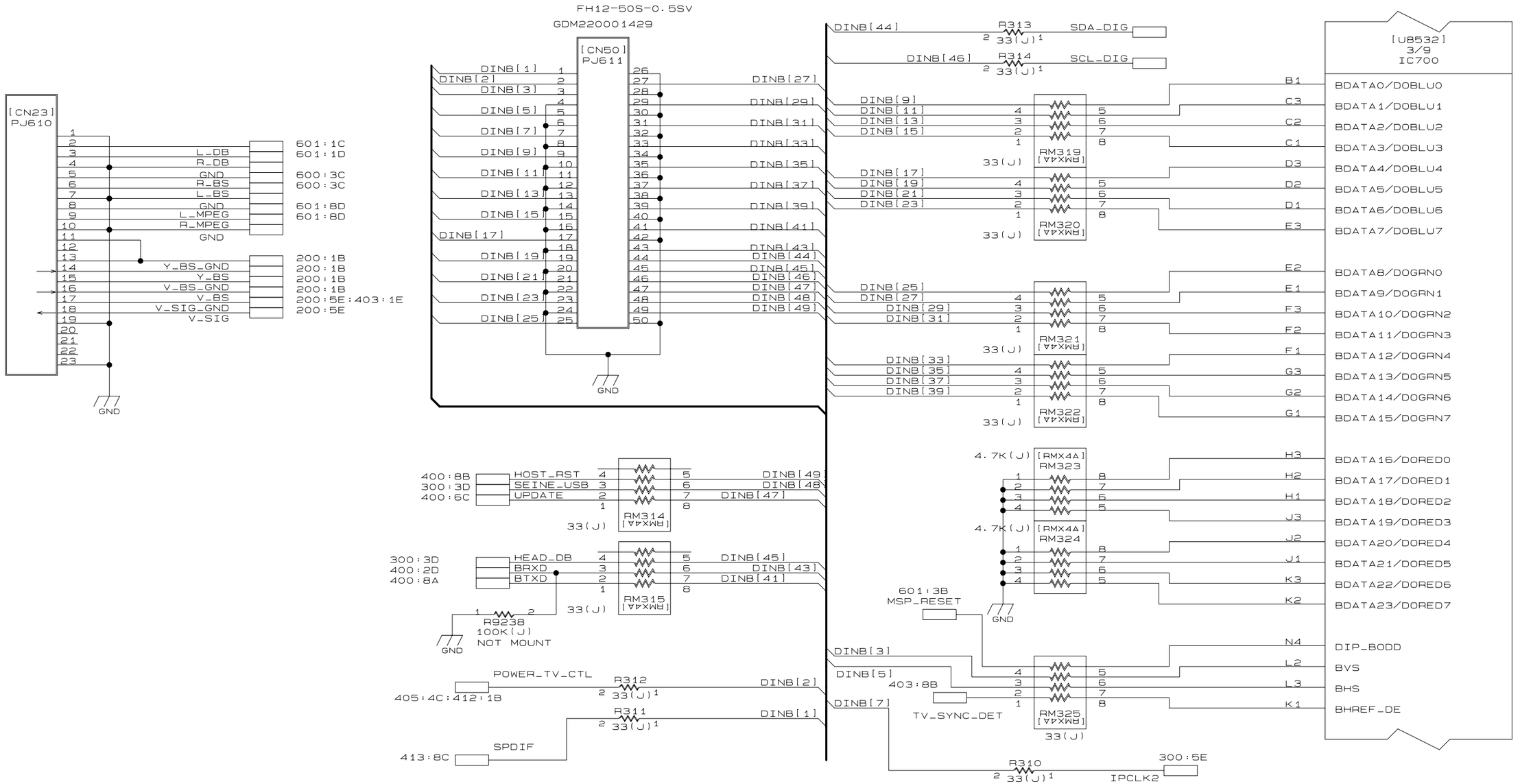
42DPC85
SIGNAL
ANALOG IN

SH. NO. 200



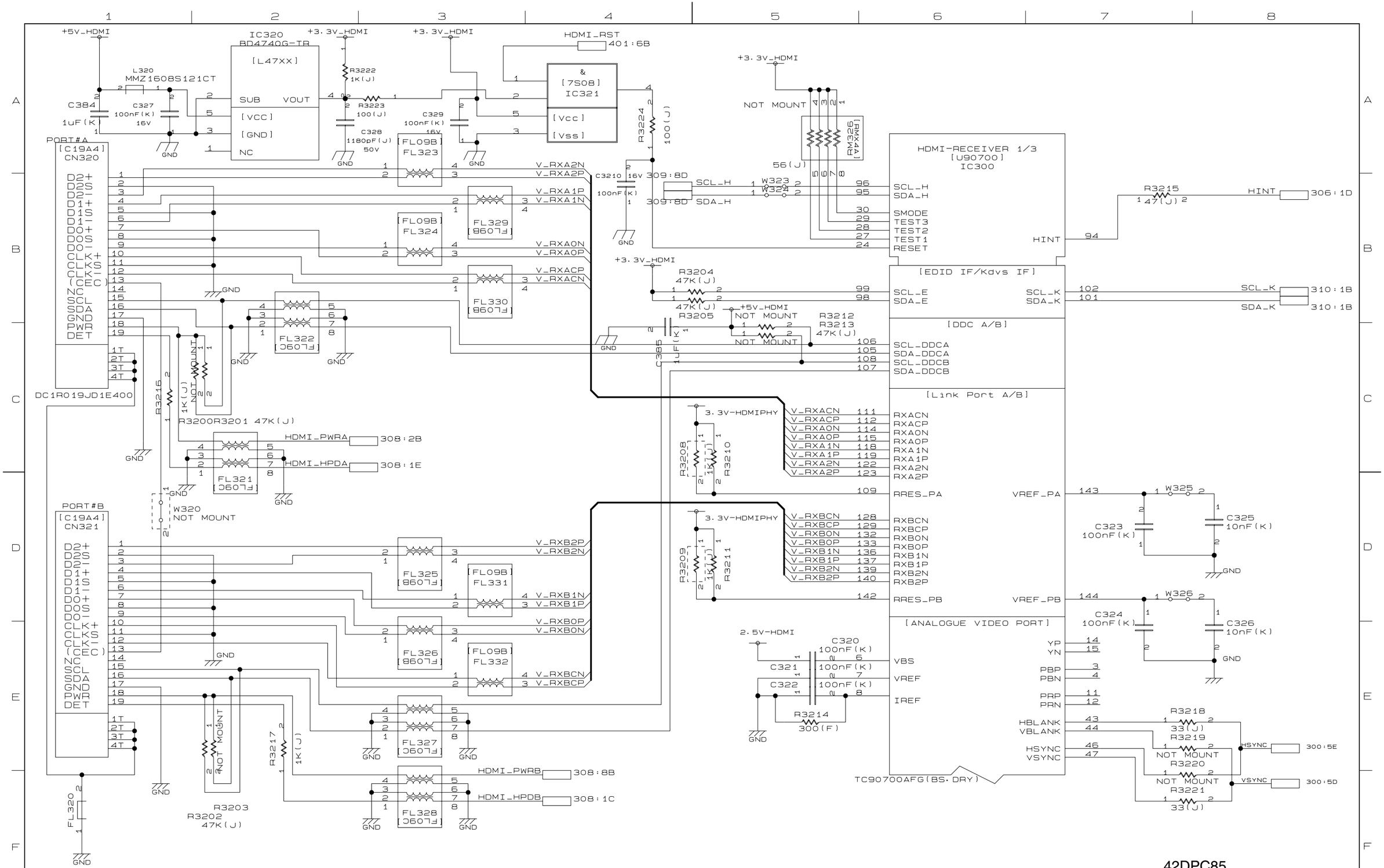
42DPC85
SIGNAL
D IN A

SH. NO.
300



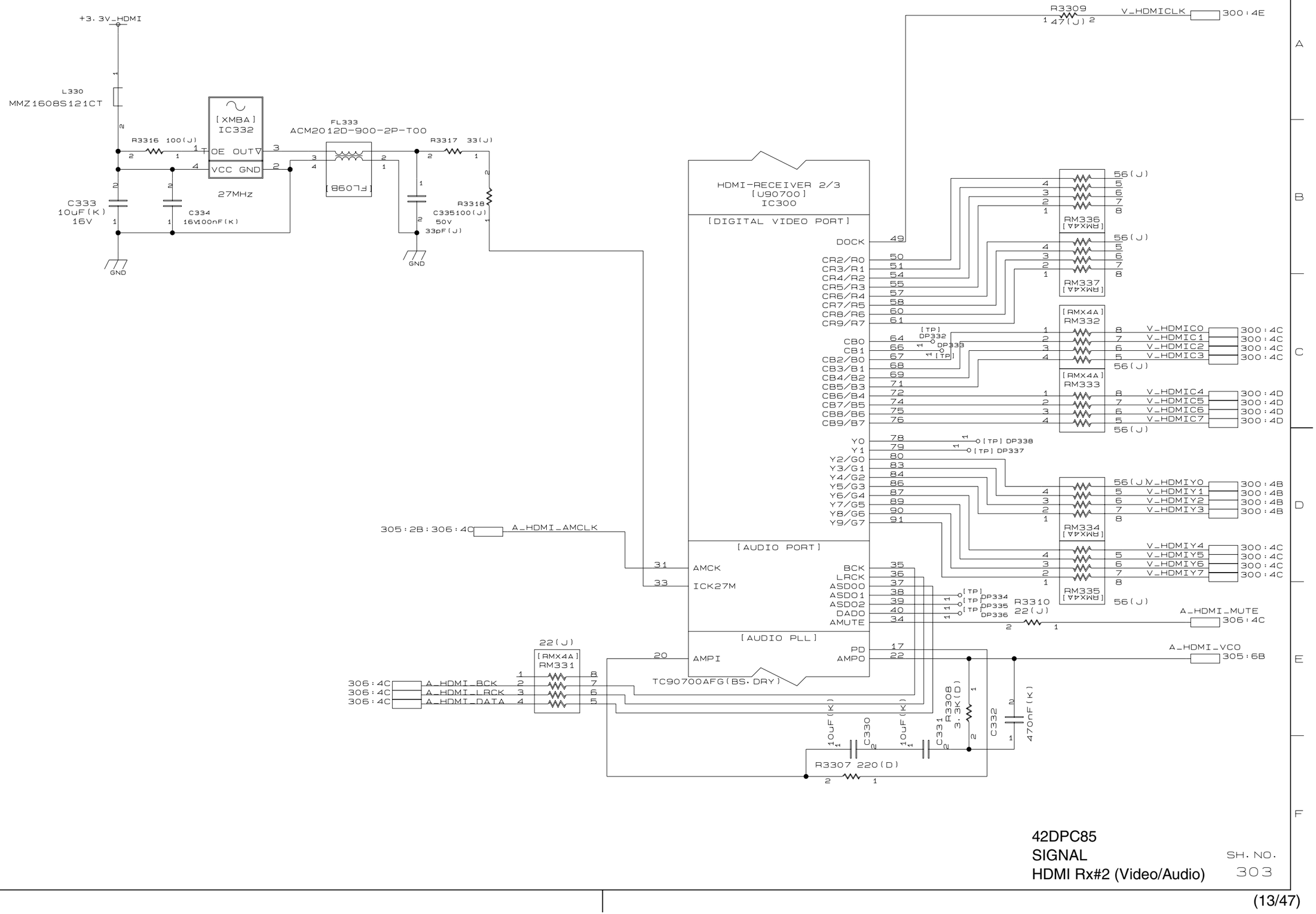
42DPC85
SIGNAL
D IN B

SH. NO.
301



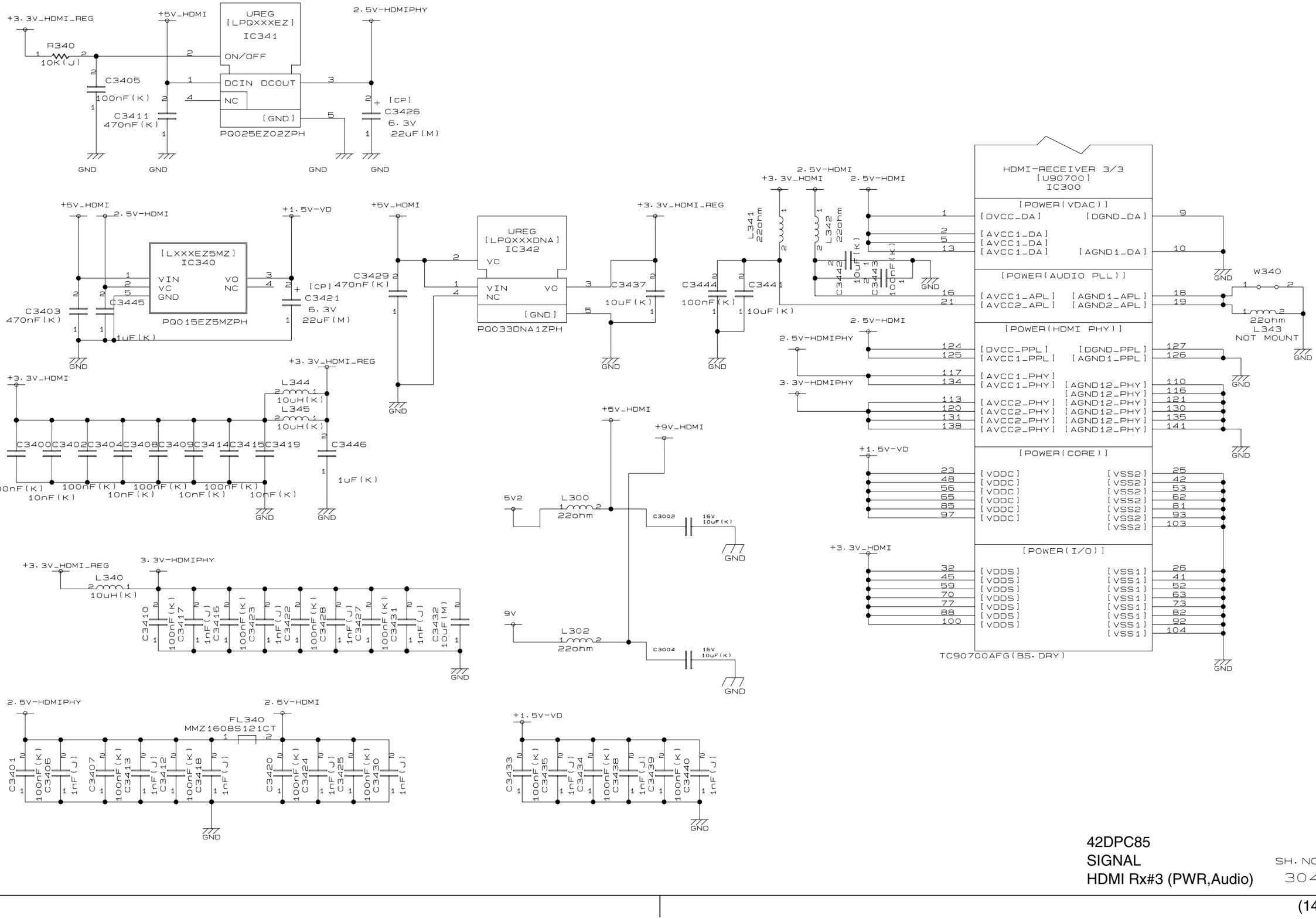
42DPC85
SIGNAL
HDMI (Link,I2C)

SH. NO.
302



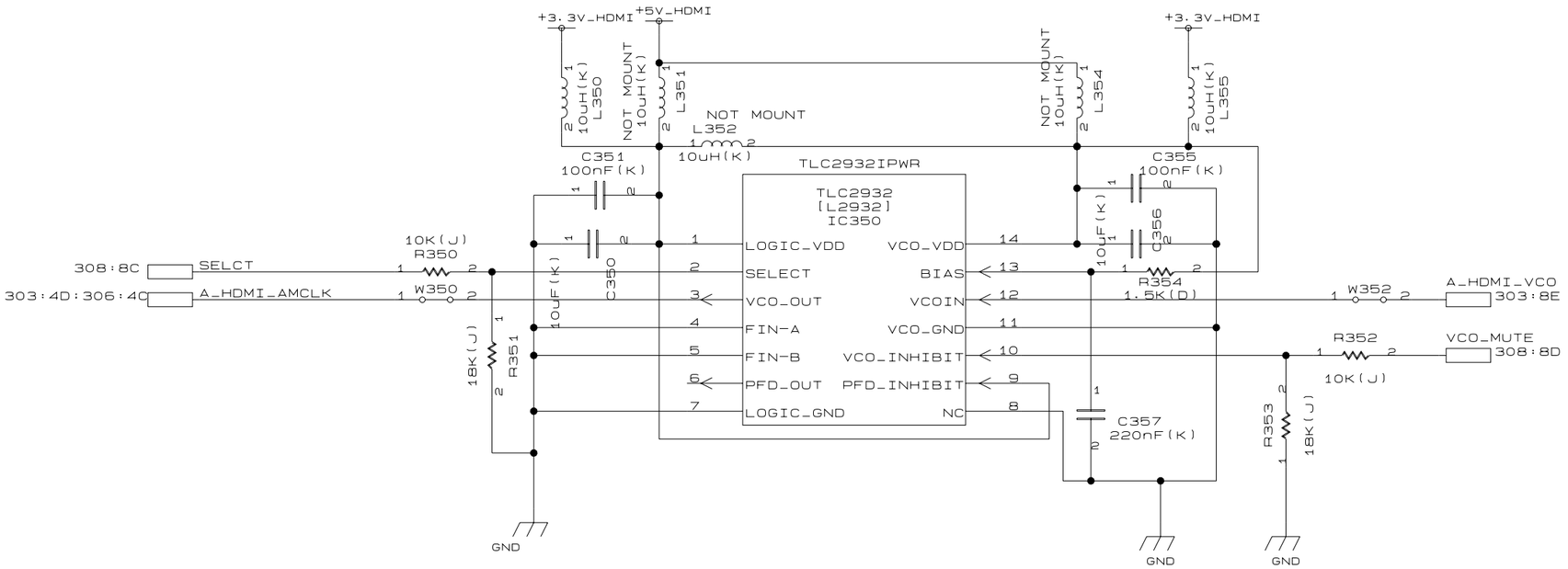
42DPC85
SIGNAL
HDMI Rx#2 (Video/Audio)

SH. NO.
303



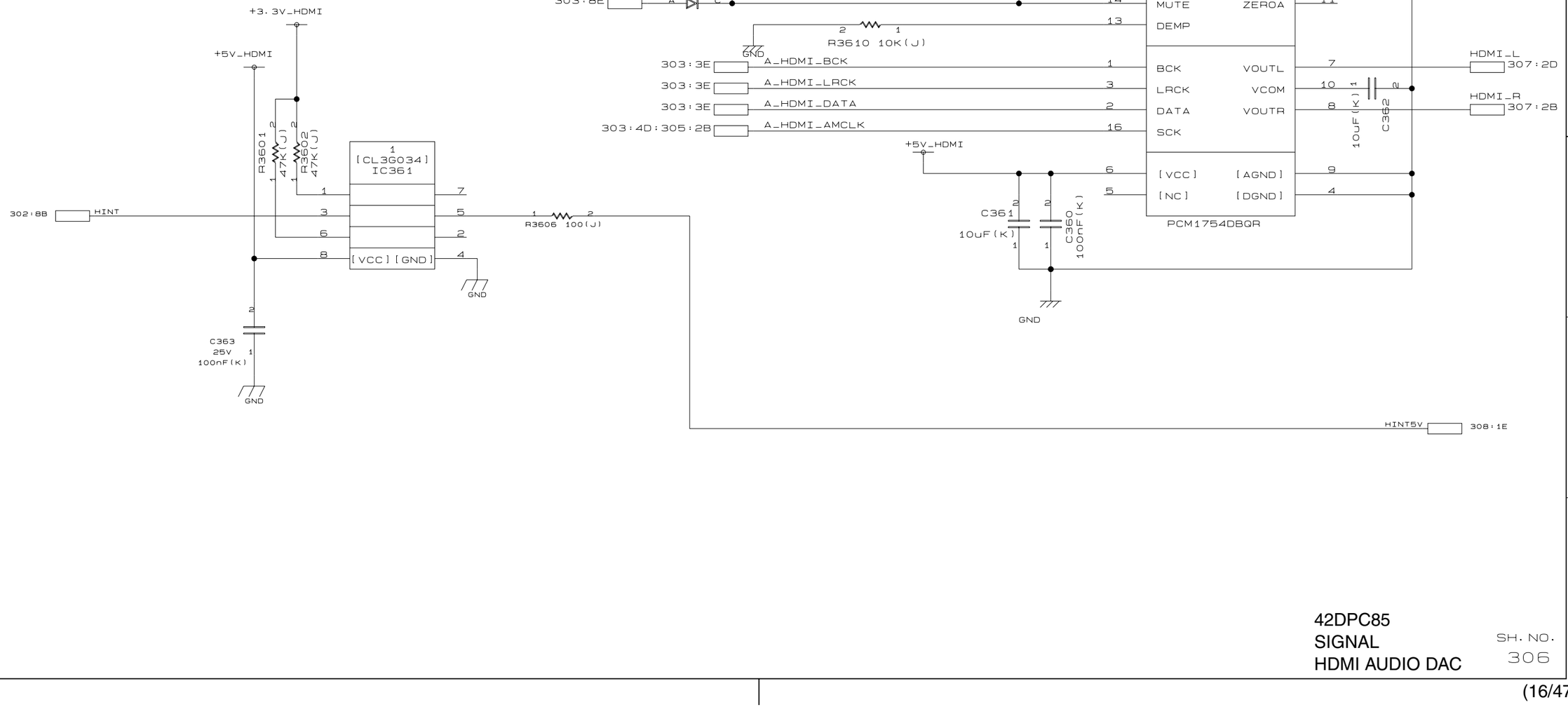
42DPC85
SIGNAL
HDMI Rx#3 (PWR,Audio)

SH. NO.
304



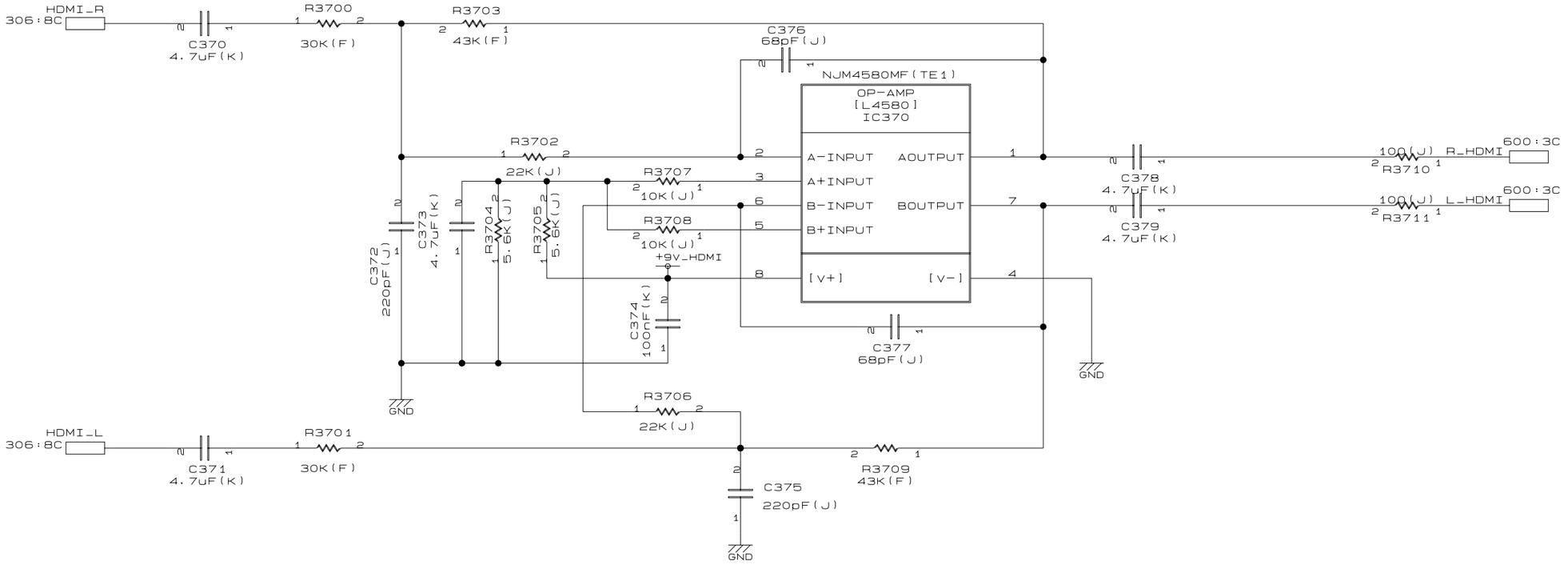
42DPC85
SIGNAL
HDMI AUDIO PLL

SH. NO.
305



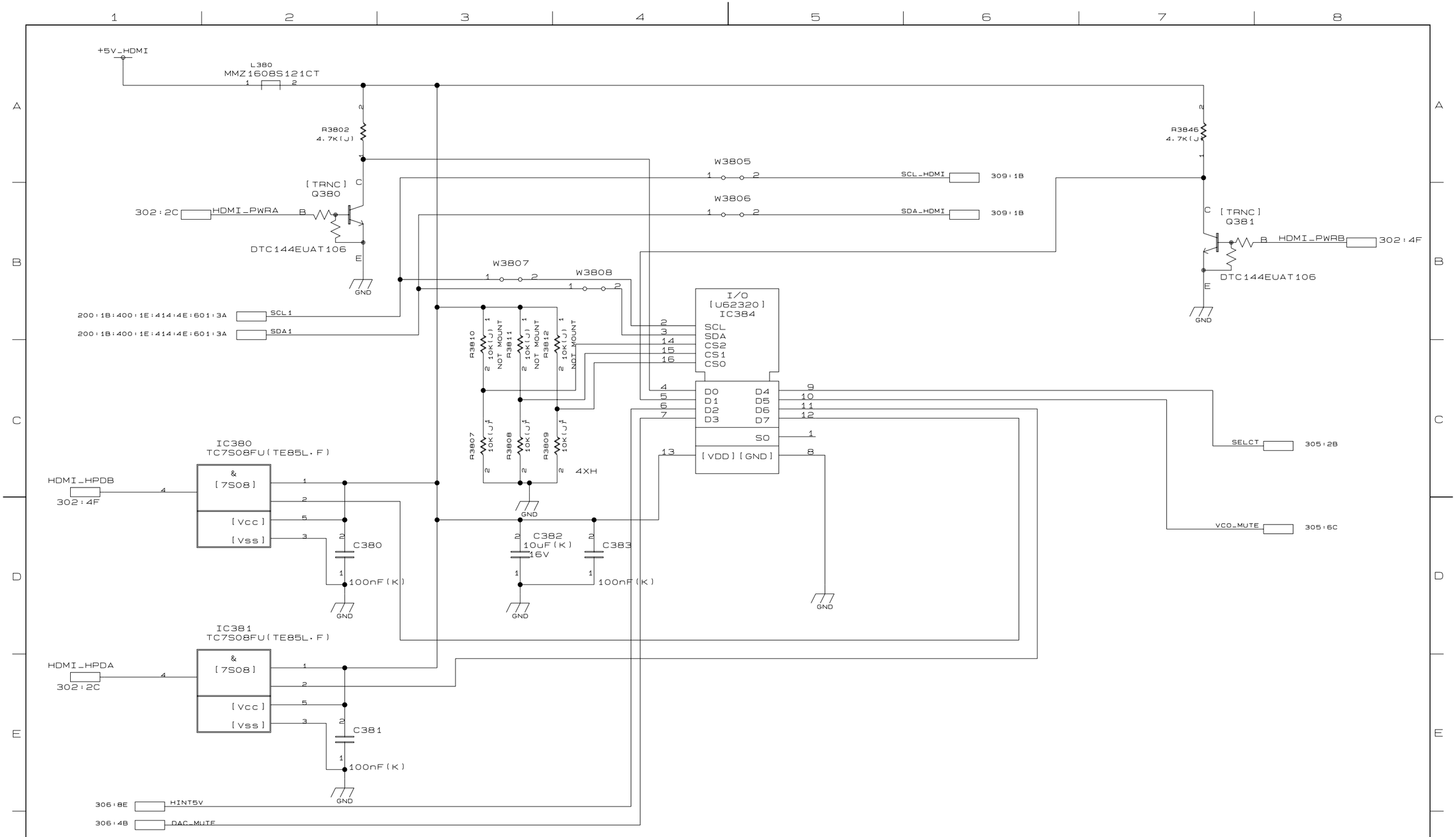
42DPC85
SIGNAL
HDMI AUDIO DAC

SH. NO.
306



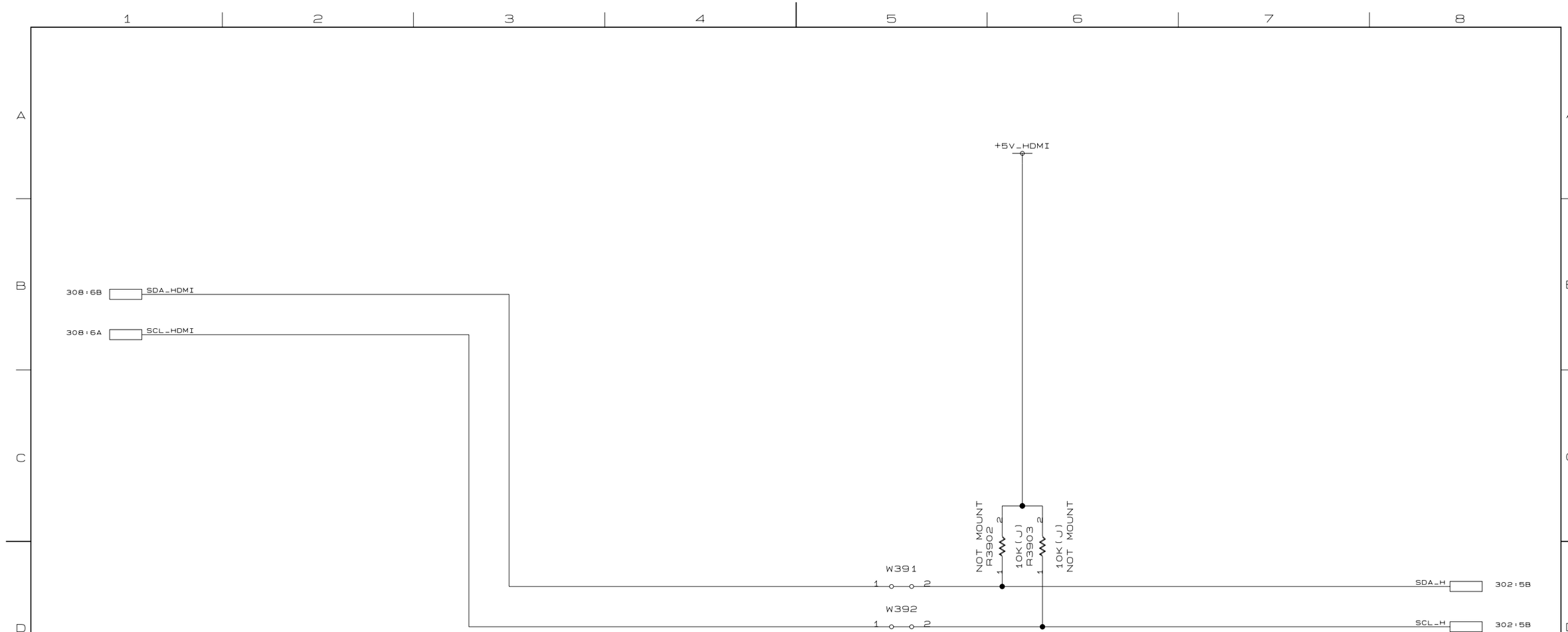
42DPC85
SIGNAL
HDMI AUDIO OUT

SH. NO.
307



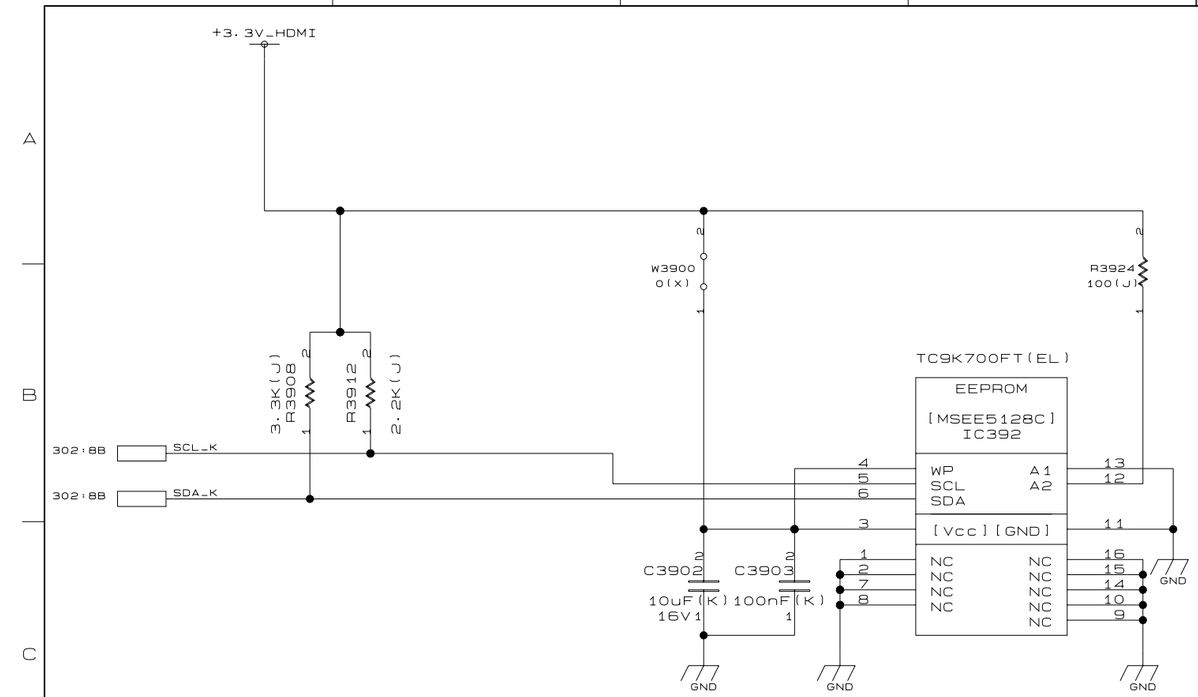
42DPC85
SIGNAL
HDMI CONTROLLER

SH. NO.
308



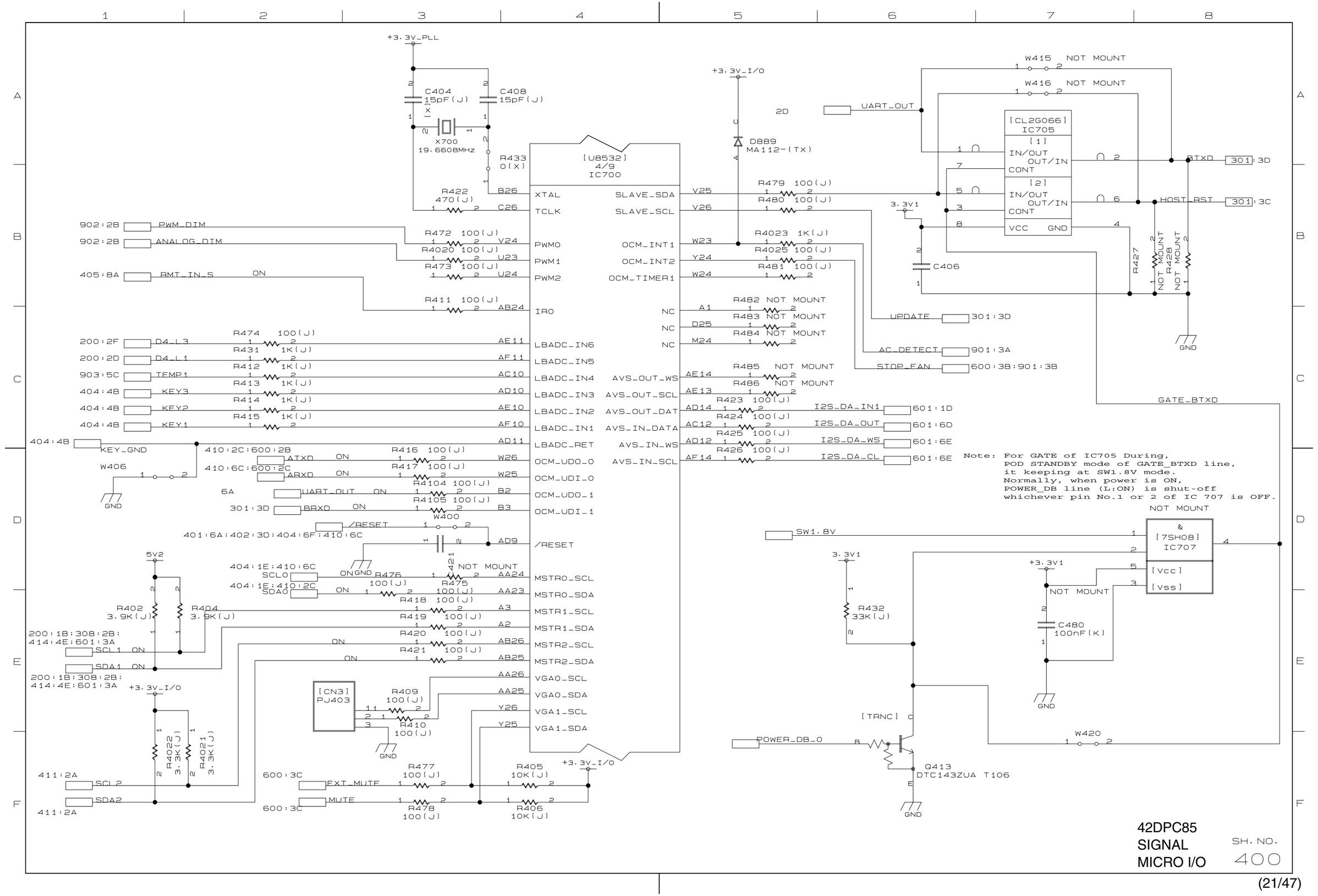
42DPC85
SIGNAL
EEPROM1

SH. NO.
309



42DPC85
SIGNAL
EEPROM2

SH. NO.
310



42DPC85
SIGNAL
MICRO I/O

SH. NO.
400

402:3A:409:1B

/OCM_RE, /OCM_WE, /ROM_CS, OCMADDR[0-20], OCMDATA[0-7]

/RESET

[U8532]
7/9
IC700

R487 33(J)
R440 33(J)

OCMADDR[20] AE15
OCMADDR21 PBIAS
OCMADDR20 PPWR
OCMADDR19 AD15
OCMADDR18 AC15 GPIO47
OCMADDR17 AF16 GPIO46
OCMADDR16 AE16 GPIO45
OCMADDR15 AD16 GPIO44
OCMADDR14 AF17 JTAG_BS_TDO
OCMADDR13 AE17 JTAG_BS_TMS
OCMADDR12 AD17
OCMADDR11 AC17 OCMCS2n
OCMADDR10 AF18 OCMCS1n
OCMADDR9 AE18 OCMCS0n
OCMADDR8 AD18 ROMCSn
OCMADDR7 AF19 OCMWEn
OCMADDR6 AE19 OCMREn
OCMADDR5 AD19
OCMADDR4 AC19
OCMADDR3 AF20
OCMADDR2 AE20
OCMADDR1 AD20
OCMADDR0 AF21

RM400 [RMX4A] 33(J): OCMADDR[19] OCMADDR[18] OCMADDR[17] OCMADDR[16]
RM401 [RMX4A] 33(J): OCMADDR[15] OCMADDR[14] OCMADDR[13] OCMADDR[12]
RM402 [RMX4A] 33(J): OCMADDR[11] OCMADDR[10] OCMADDR[9] OCMADDR[8]
RM403 [RMX4A] 33(J): OCMADDR[7] OCMADDR[6] OCMADDR[5] OCMADDR[4]
RM404 [RMX4A] 33(J): OCMADDR[3] OCMADDR[2] OCMADDR[1] OCMADDR[0]

U26 R441 100(J) BL_ONOFF 902:4D
U25 R442 100(J) POWER_LCD 901:3B
R25 R443 NOT MOUNT D4_SW 200:4D
R24 R444 100(J) NOT MOUNT
R23 R445 100(J) HDMI_RST 302:4A
T26 R446 100(J) POWER_KEY_S 405:8A
T25 R447 100(J) GP1 200:1C
T24 R448 100(J) BS_TV_MUTE 600:3C
AC24 R488 100(J) USB 300:1D
AD26 R489 100(J) MAIN_SYNC_DET 403:8E
AD25 R449 1K(J) MONI_SW 200:2E

RM407 [RMX4A] 33(J): AD24, AC26, AC25 → /ROM_CS, /OCM_WE, /OCM_RE

RM405 [RMX4A] 33(J): AE26 OCMDATA0, AE25 OCMDATA1, AE24 OCMDATA2, AF26 OCMDATA3 → OCMDATA[0] OCMDATA[1] OCMDATA[2] OCMDATA[3]
RM406 [RMX4A] 33(J): AF25 OCMDATA4, AF24 OCMDATA5, AC23 OCMDATA6, AD23 OCMDATA7 → OCMDATA[4] OCMDATA[5] OCMDATA[6] OCMDATA[7]

AE23 OCMDATA8 R4029 100(J) MONI_SW2 600:3B
AF23 OCMDATA9 R4030 NOT MOUNT AV/S_DET2 200:4F
AD22 OCMDATA10 R4031 NOT MOUNT
AE22 OCMDATA11 R4032 1K(J)
AF22 OCMDATA12 R4033 100(J) SW_DVI2 600:3B
AC21 OCMDATA13 R4034 100(J) LED3 404:8B
AD21 OCMDATA14 R4035 100(J) LED4 404:8B
AE21 OCMDATA15 R4036 100(J) 50HZ_60HZ 903:1A

+3.3V1
NOT MOUNT
C4014 100nF(K)
GND
NOT MOUNT
& [7SH08] IC421 [Vcc] [Vss]
W417
NOT MOUNT
STBY_RST 405:3B

+3.3V_I/O
R40710K(J)
NOT MOUNT R4118 10K(J)
R4119 10K(J) NOT MOUNT
GND

+3.3V_I/O
R4026 1K(J)
[TRP] Q404 2SA1586-Y(TE85L.F)
R4027 1K(J)
300:4E:901:3B
POWER_SIG_DET
R4028 47K(J)
C478 470pF(J)
GND

5V2
BD5245G-TR
[LBD52XXA] IC424
CT VOUT [VCC] [GND] NC
C4020 100nF(K) 16V
C4019 4.7nF(K)
GND

42DPC85
SIGNAL
OCM MEMORY I/F

SH. NO. 401

401:1A:409:1B

/OCM_RE. /OCM_WE. /ROM_CS. OCMADDR[0-20]. OCMDATA[0-7]

MX26LV800TTC-70G

[MFLV800AT]
IC404

OCMADDR[1] 25
OCMADDR[2] 24
OCMADDR[3] 23
OCMADDR[4] 22
OCMADDR[5] 21
OCMADDR[6] 20
OCMADDR[7] 19
OCMADDR[8] 18
OCMADDR[9] 8
OCMADDR[10] 7
OCMADDR[11] 6
OCMADDR[12] 5
OCMADDR[13] 4
OCMADDR[14] 3
OCMADDR[15] 2
OCMADDR[16] 1
OCMADDR[17] 48
OCMADDR[18] 17
OCMADDR[19] 16

A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18

/ROM_CS 26 → CE
/OCM_RE 28 → OE
/OCM_WE 11 → WE
47 → BYTE
12 → RESET

RY/BY → 15

GND

/RESET 1 2 W411 MOUNT
400:3D:401:6A:404:6F:410:6C

1 2 R4086 2.2K(J)

2 C464 NOT MOUNT 1
GND

+3.3V_I/O

OCMADDR[20]

NC 13
NC

37 VCC

VSS 27
VSS 46

2 C410 10uF(K) 6.3V 1

2 C411 100nF(K) 16V 1

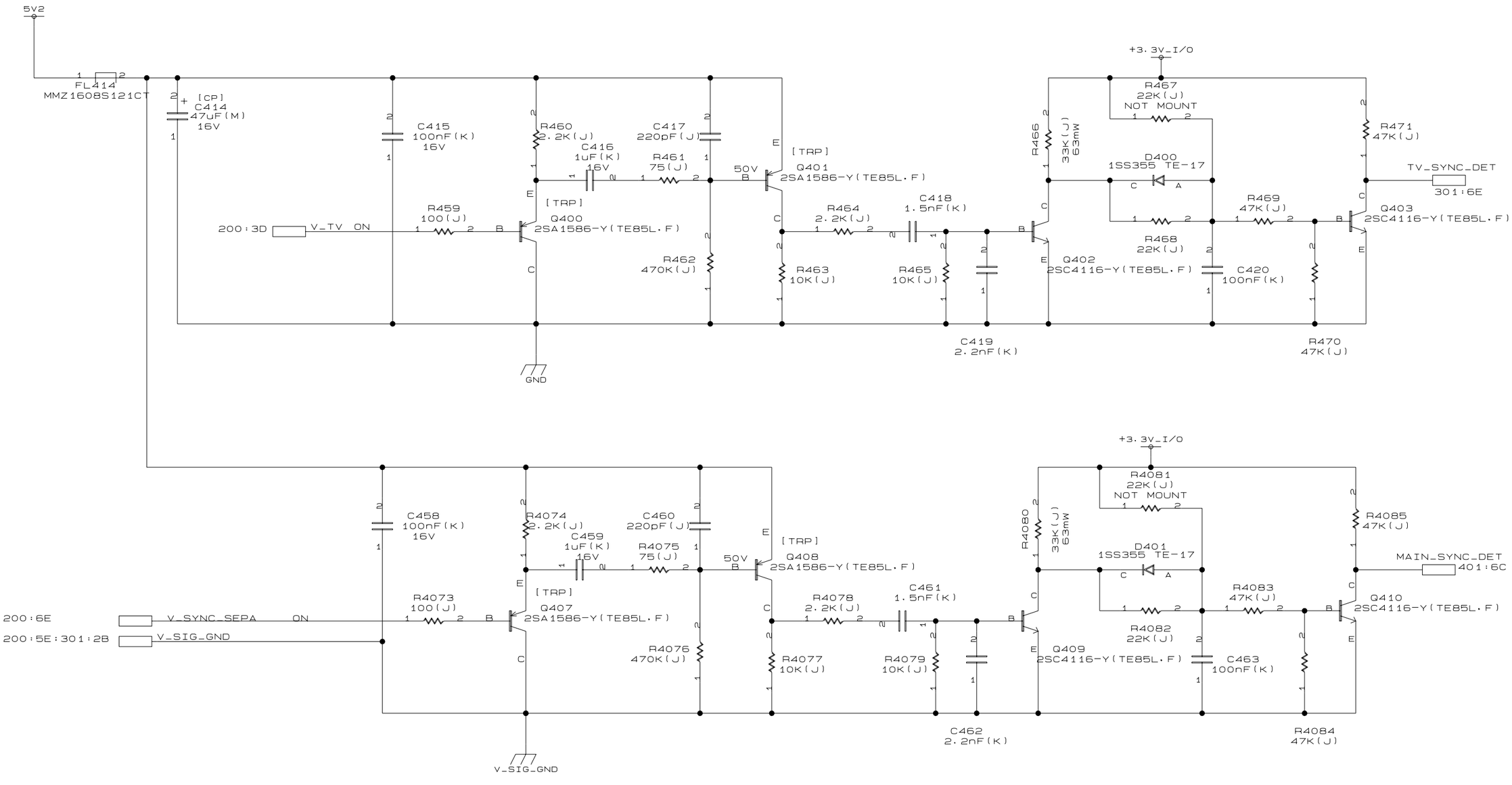
GND

DQ0 29 OCMDATA[0]
DQ1 31 OCMDATA[1]
DQ2 33 OCMDATA[2]
DQ3 35 OCMDATA[3]
DQ4 38 OCMDATA[4]
DQ5 40 OCMDATA[5]
DQ6 42 OCMDATA[6]
DQ7 44 OCMDATA[7]
DQ8 30
DQ9 32
DQ10 34
DQ11 36
DQ12 39
DQ13 41
DQ14 43
DQ15/A-1 45 OCMADDR[0]

NC 10
NC 14

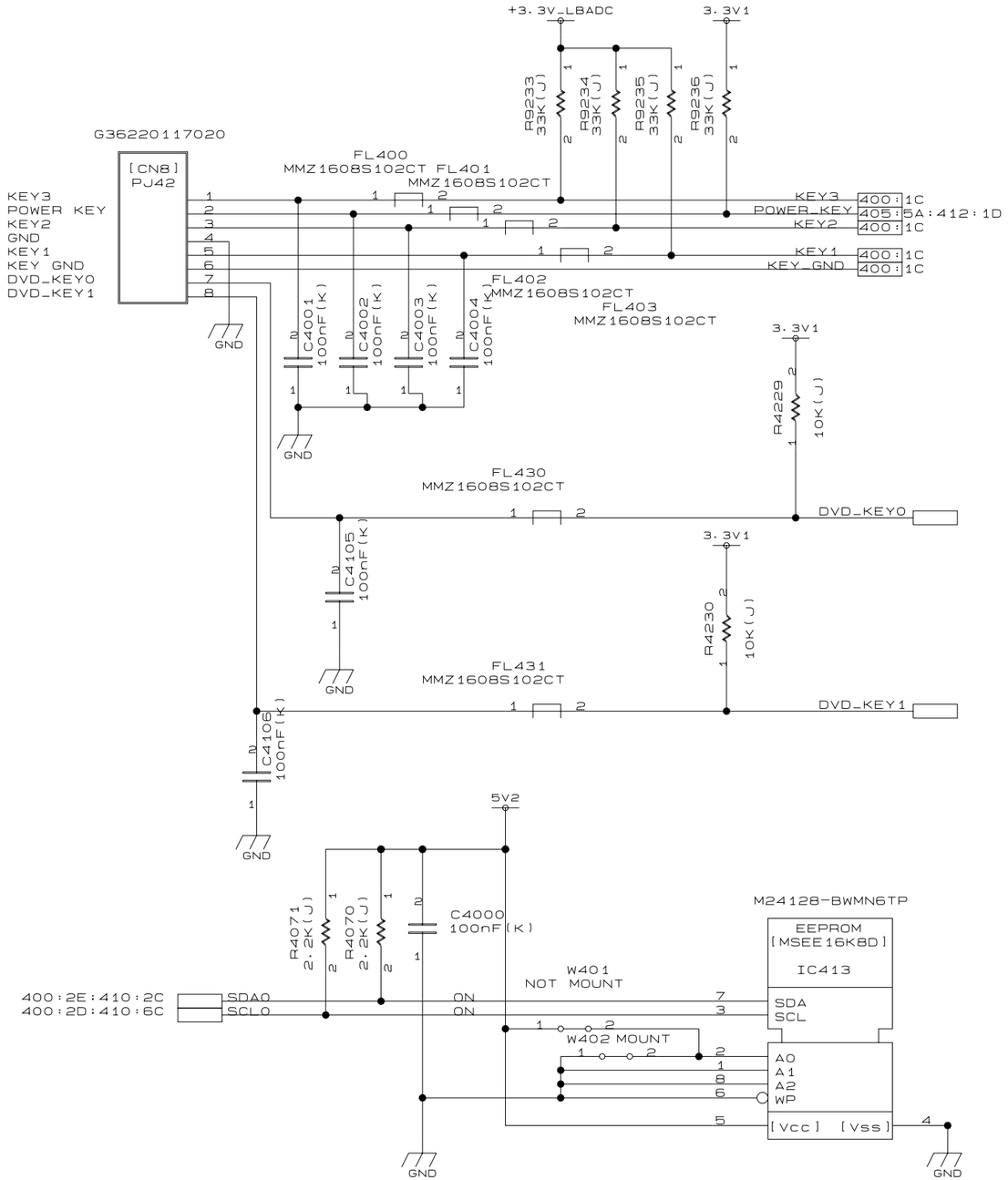
42DPC85
SIGNAL
FLASH MEMORY

SH. NO.
402



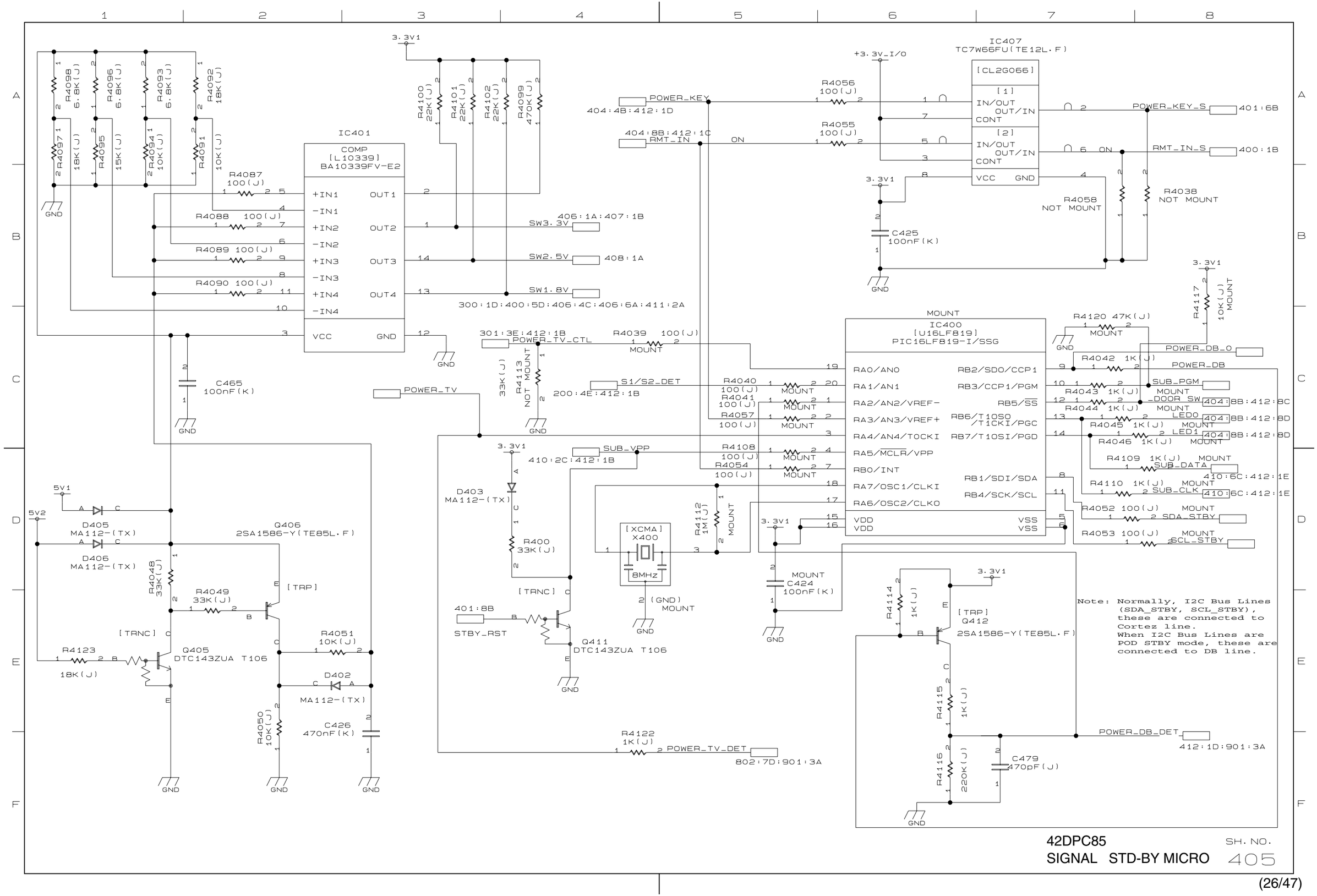
42DPC85
SIGNAL
SYNC SEPA

SH. NO.
403



42DPC85
SIGNAL
E2P OTHER

SH. NO.
404



42DPC85
SIGNAL STD-BY MICRO

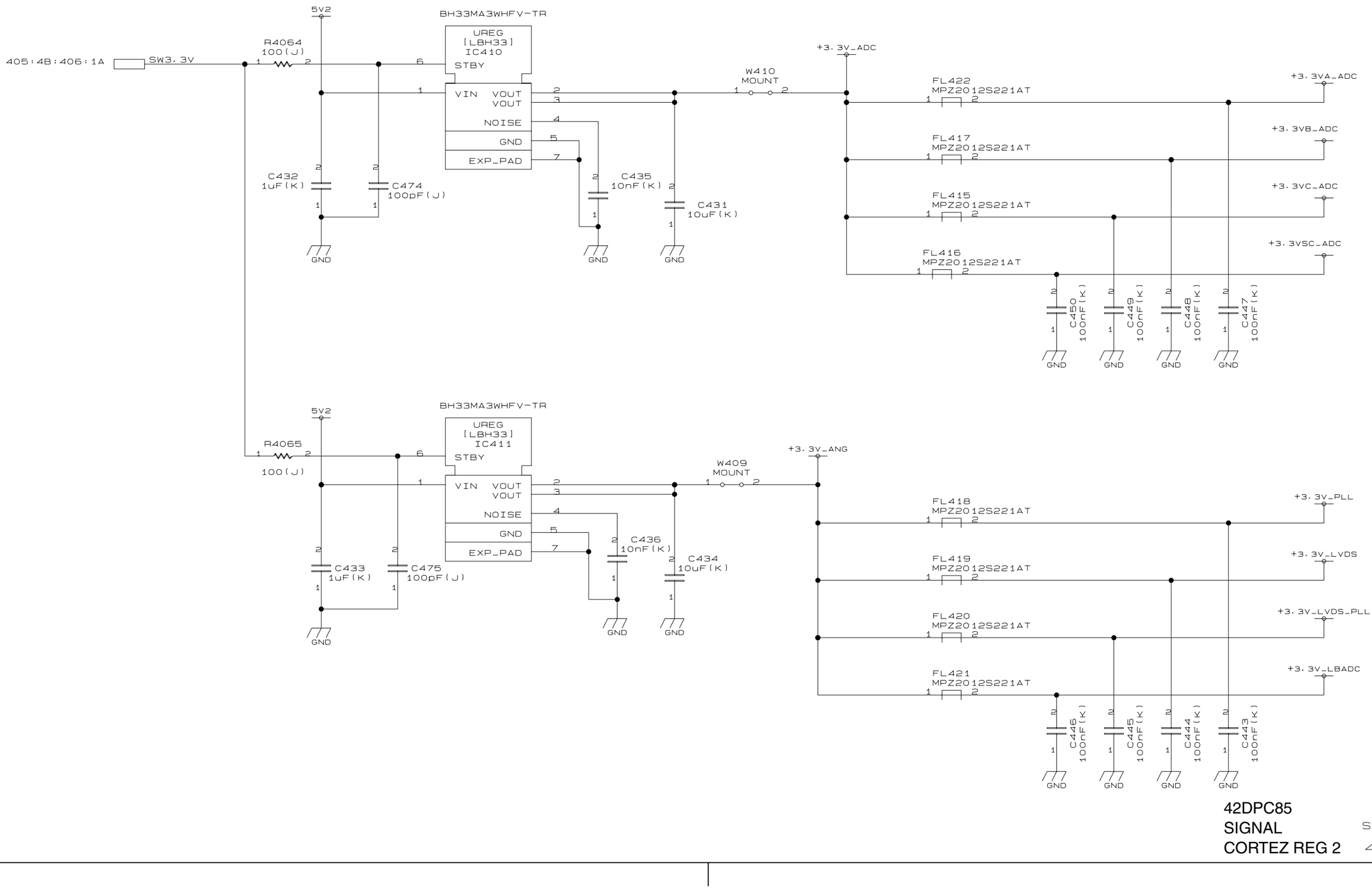
SH. NO. 405

405:4B:407:1B

SW3.3V
R4059
100(J)
C471
47pF(J)
GND
IC402
PQ070XH02ZPH
UREG
[LPQ070XH]
3.3V
VC
VADJ
VIN
VO
[GND]
5V2
C428
1uF(K)
GND
+3.3V
[CP]
C427
47uF(M)
GND
FL423
MPZ2012S221AT
+3.3V_I/O
C451
100nF(K)
16V
GND
150(F)
1.5K(F)
R4062
R4061
R4060
1K(F)
GND
300:1D:400:5D:405:4B:6A:411:2A
R4063
100(J)
SW1.8V
C473
100pF(J)
GND
PQ018EH02ZPH
UREG
[LPQXXXEH]
IC403
1.8V
VC
NC
VIN
VO
[GND]
+3.3V
C429
1uF(K)
GND
[CP]
C453
147uF(M)
GND
300:1D:400:5D:405:4B:4C:411:2A
5V2
SW1.8V
R4072
100(J)
BH18LB1WG-TR
UREG
[LBHXX]
IC412
STBY
NC
VIN
VOUT
[GND]
C452
100nF(K)
GND
C472
100pF(J)
+1.8V_ADC
FL427
MPZ1608S221AT
C454
1uF(K)
GND
NOT MOUNT
FL428
MPZ1608S221AT
+1.8V_DLL
FL425
MPZ1608S221AT
+1.8V_PLL
FL426
MPZ1608S221AT
+1.8V_CORE
FL424
MPZ1608S221AT
C430
100nF(K)
C456
100nF(K)
C455
100nF(K)
GND

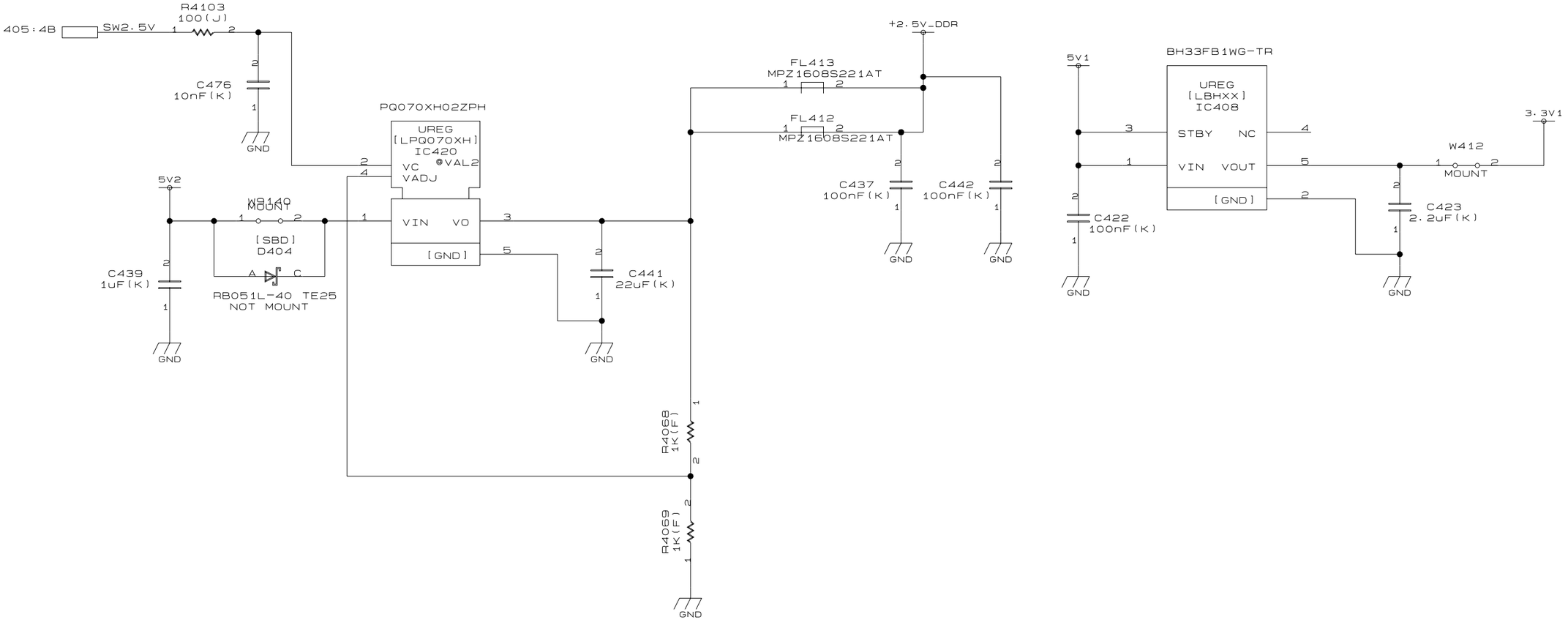
42DPC85
SIGNAL
CORTEZ REG 1

SH. NO.
406



42DPC85
SIGNAL
CORTEZ REG 2

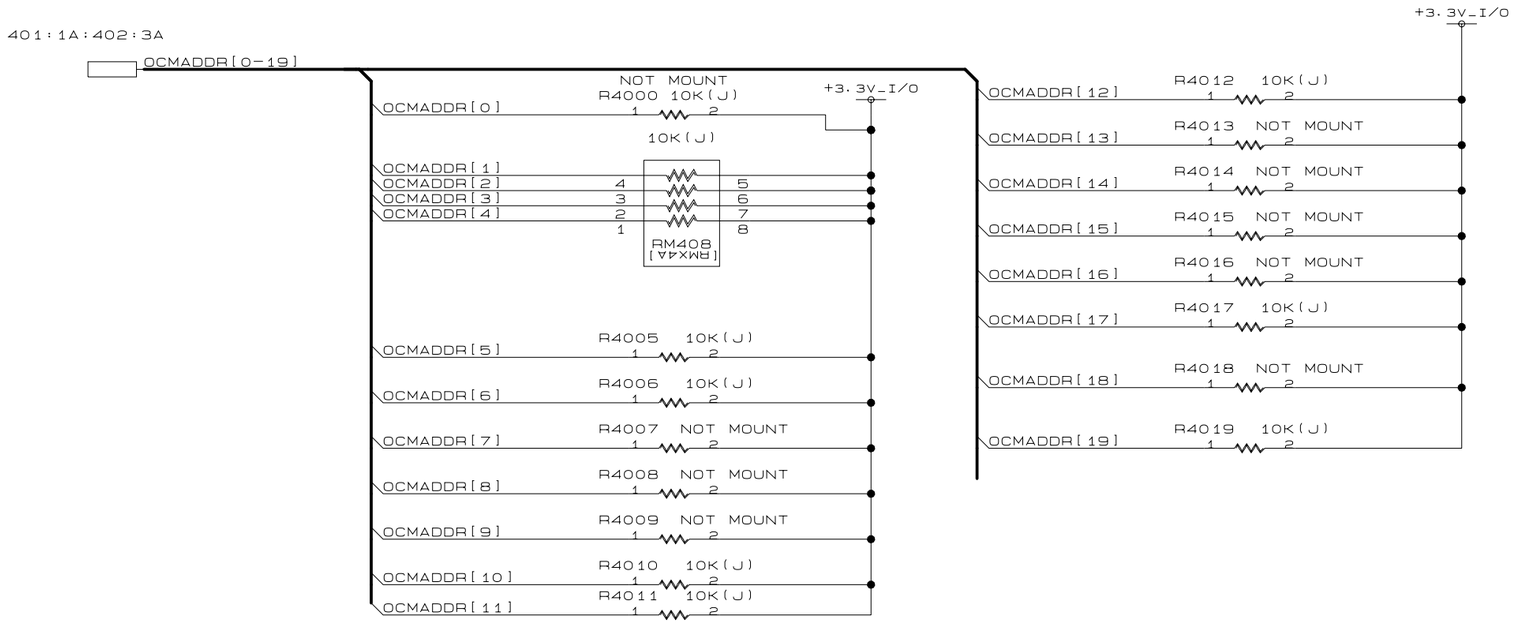
SH. NO.
407



42DPC85
SIGNAL
CORTEZ REG 3

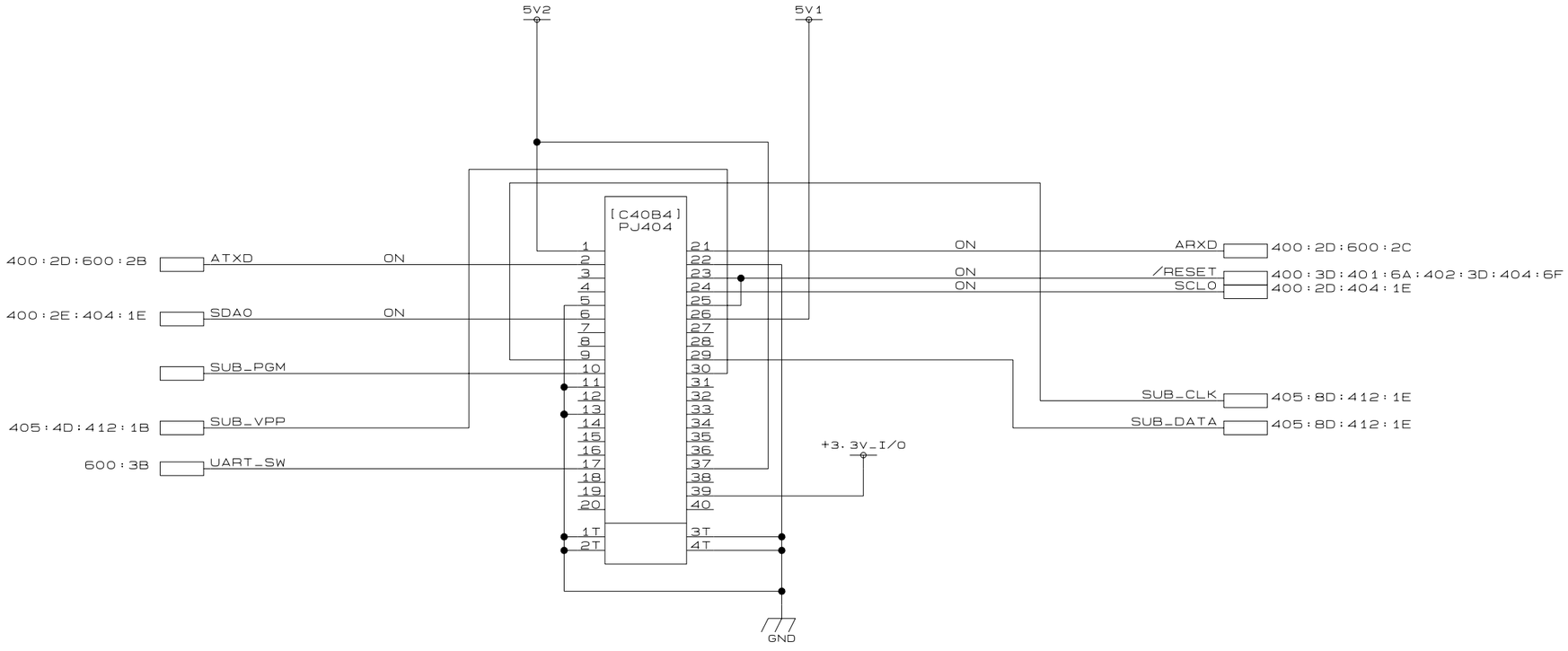
SH. NO.
408

401:1A:402:3A



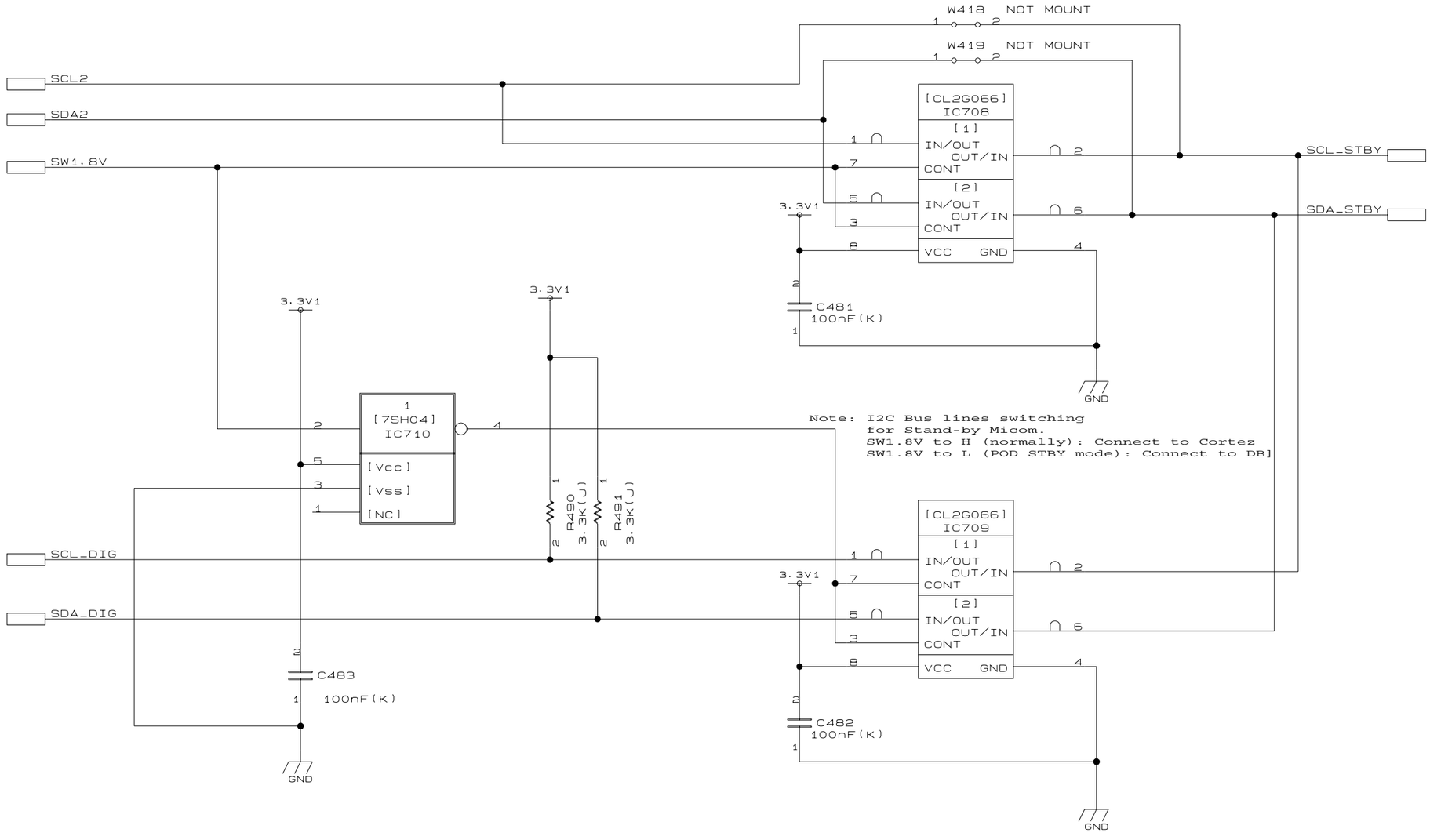
42DPC85
SIGNAL
BOOT CONFIG

SH. NO.
409



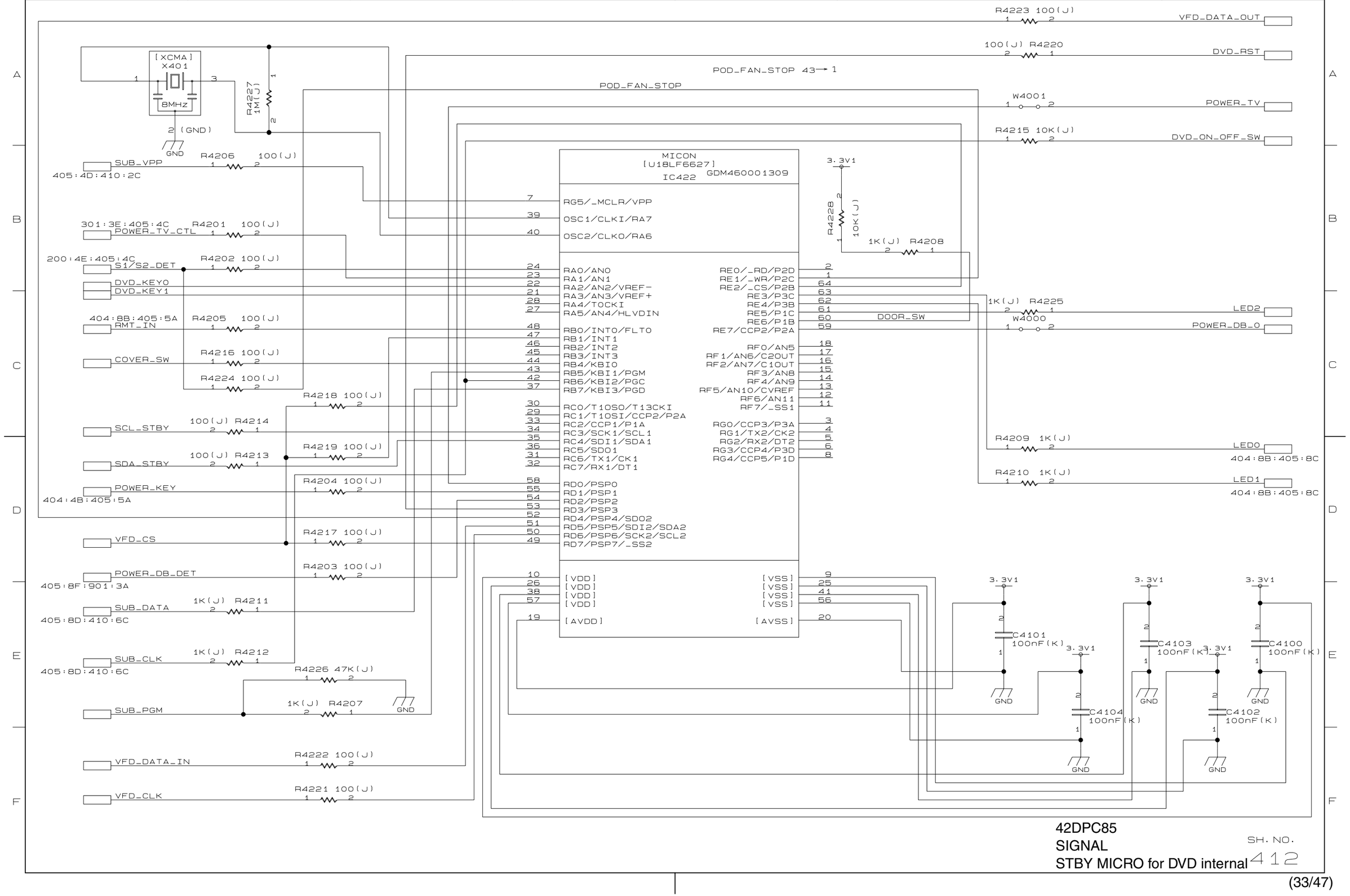
42DPC85
SIGNAL
SERVICE CONNECTOR

SH. NO.
410



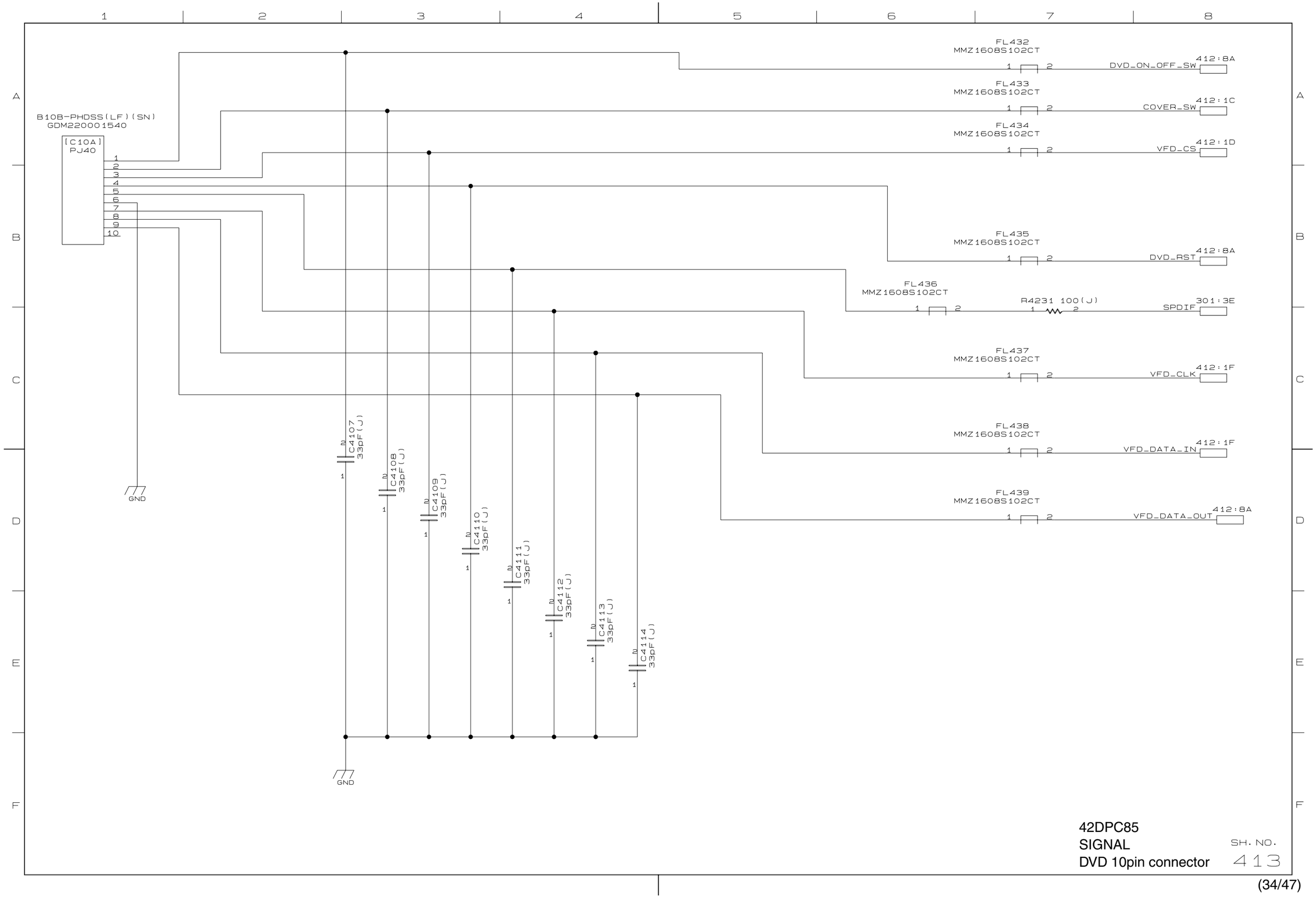
42DPC85
SIGNAL
I2C Switch

SH. NO.
411



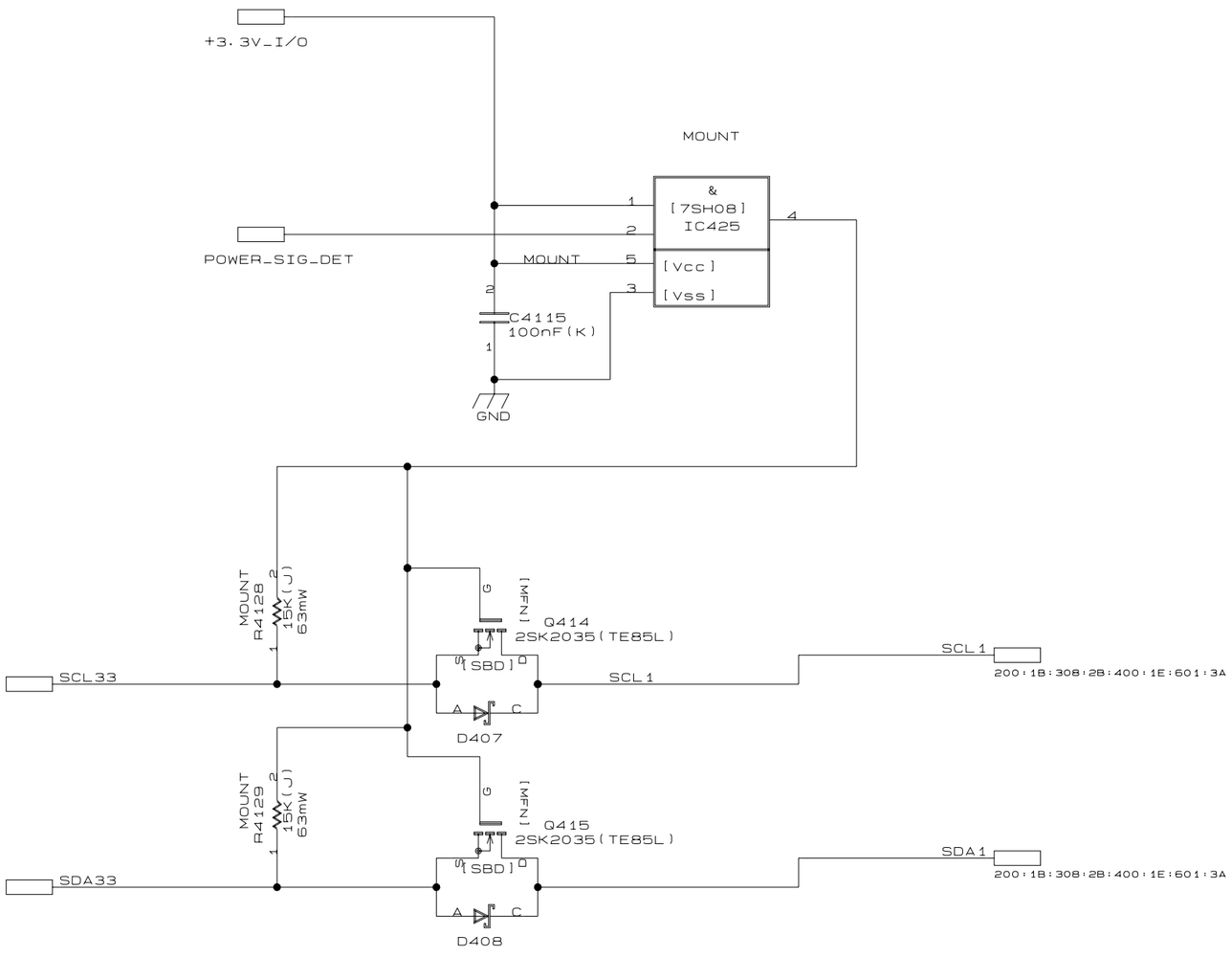
42DPC85
SIGNAL
STBY MICRO for DVD internal

SH. NO. 412



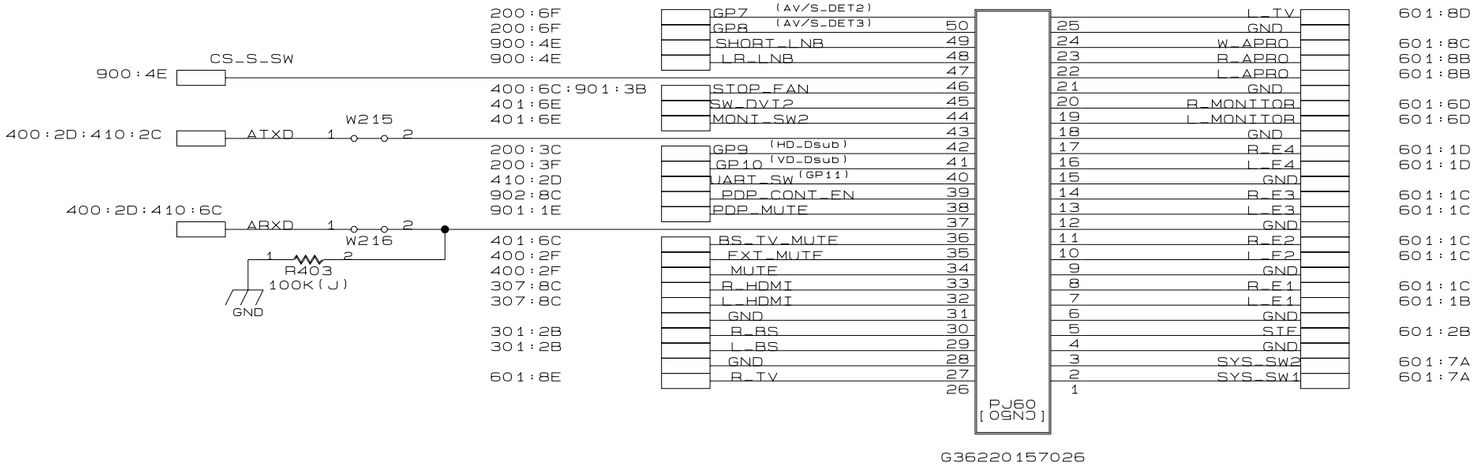
42DPC85
SIGNAL
DVD 10pin connector

SH. NO.
413



42DPC85
SIGNAL
I2C Level shift

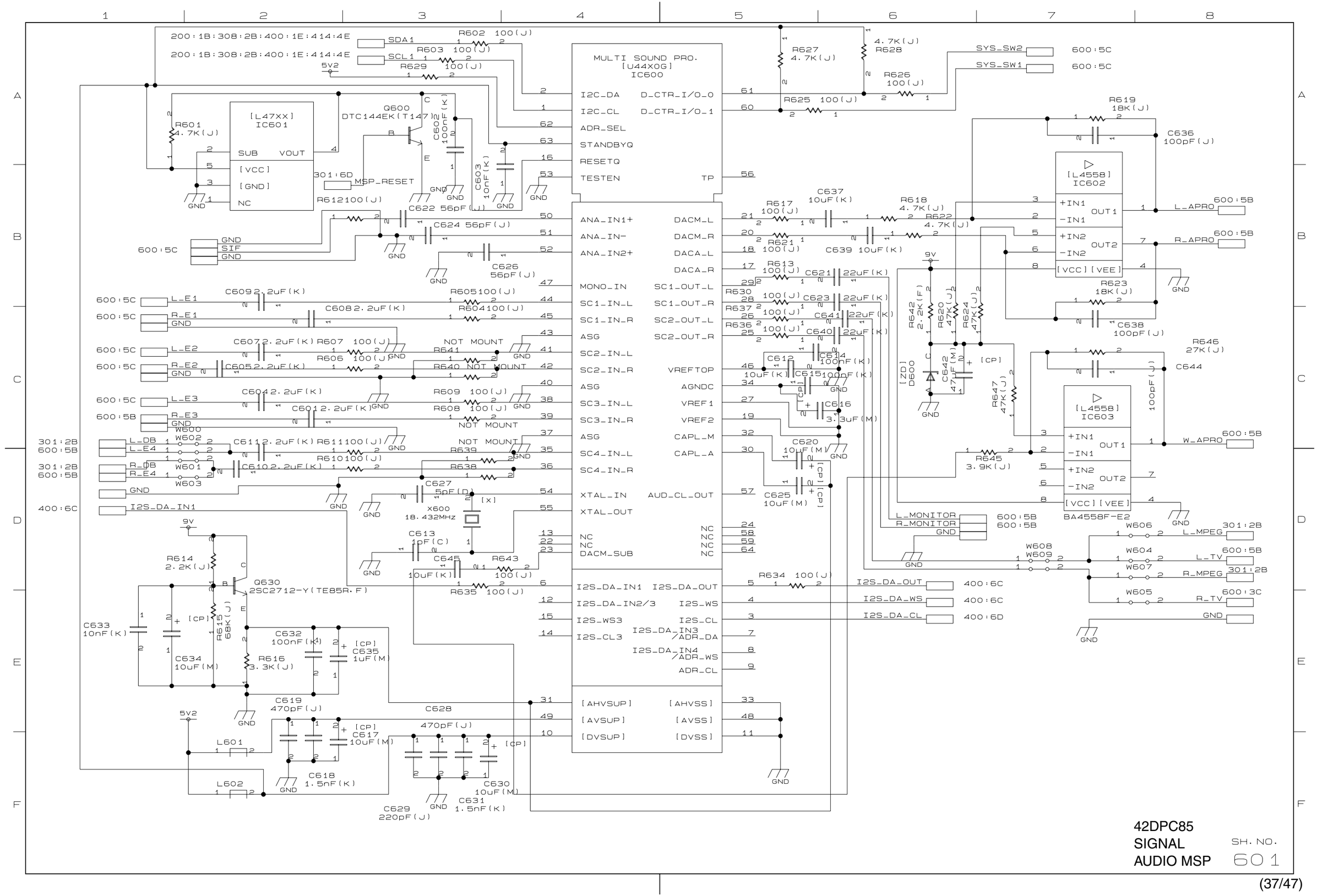
SH. NO.
414



G36220157026

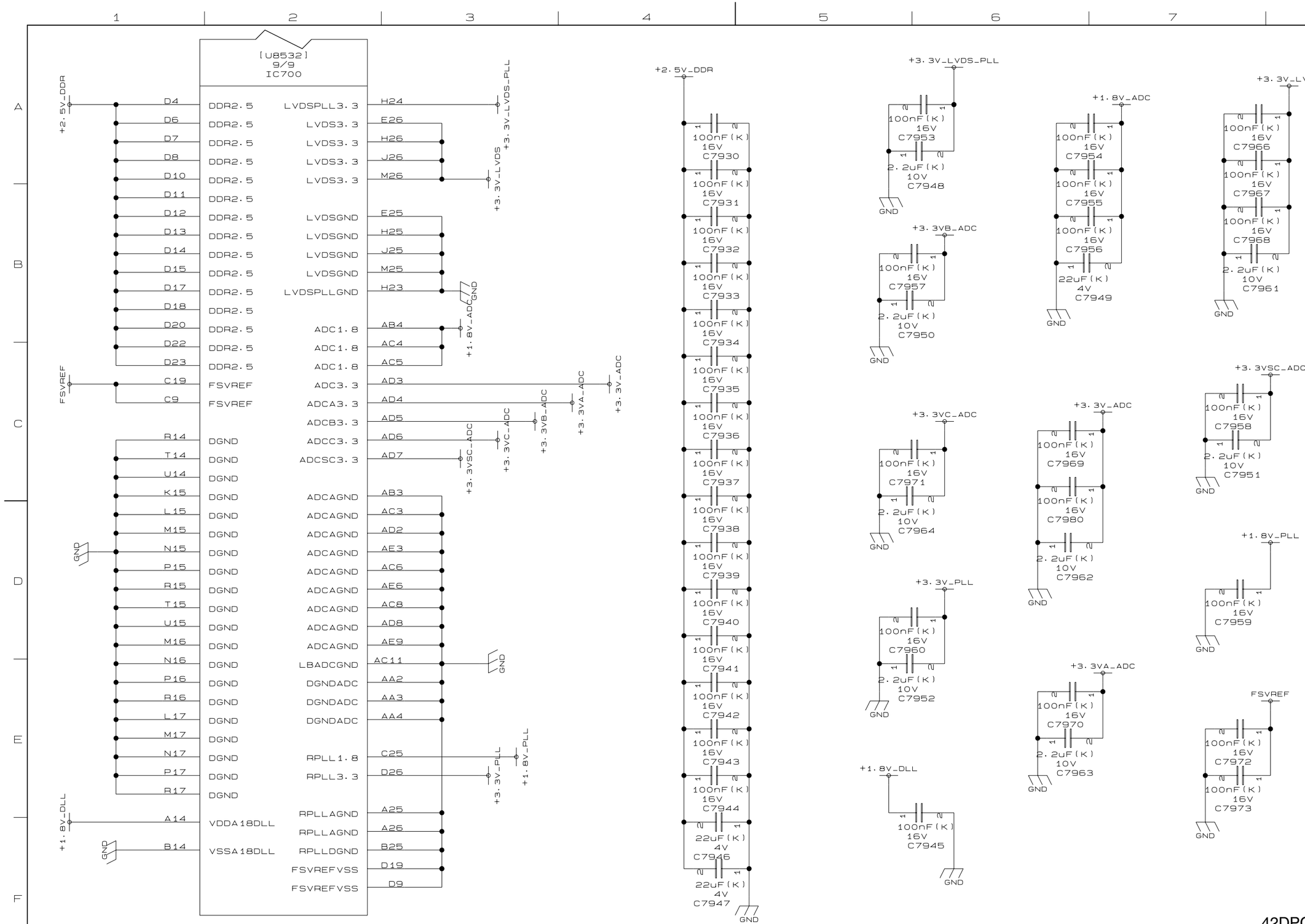
42DPC85
SIGNAL
AUDIO

SH. NO.
600



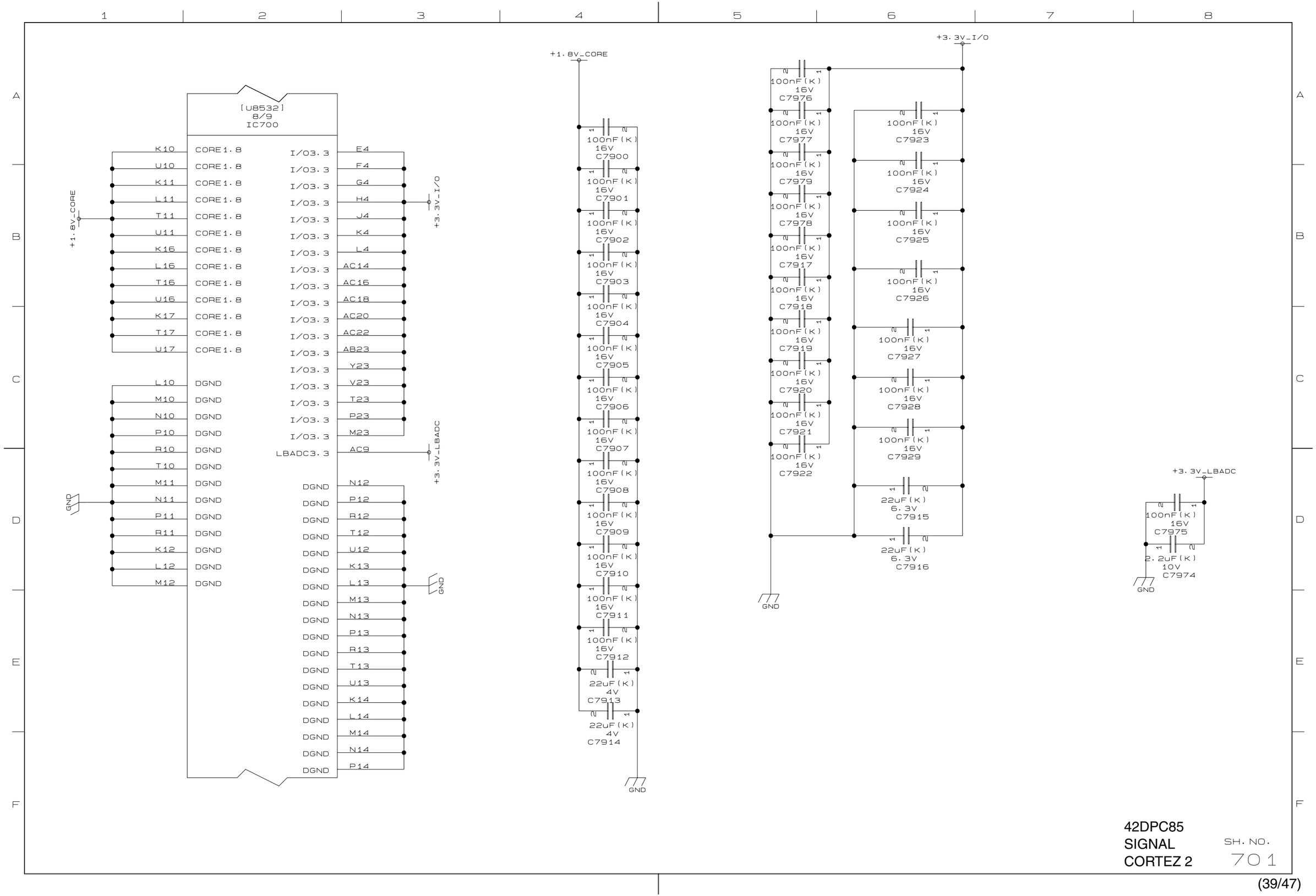
42DPC85
SIGNAL
AUDIO MSP

SH. NO.
601



42DPC85
SIGNAL
CORTEZ 1

SH. NO.
700



42DPC85
SIGNAL
CORTEZ 2

SH. NO.
701

/FSCAS. /FSCS[0-1]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-31]. FSDQM[0-3]. FSDQS[0-3]

[U8532]
5/9
IC700

FSDATA[15] FSDATA[14] FSDATA[13] FSDATA[12] — RM700 [RMX4A] 33(J) — B4 A4 B5 A5
FSDATA[11] FSDATA[10] FSDATA[9] FSDATA[8] — RM701 [RMX4A] 33(J) — B7 A7 B8 A8
FSDATA[7] FSDATA[6] FSDATA[5] FSDATA[4] — RM702 [RMX4A] 33(J) — B9 A9 B10 A10
FSDATA[3] FSDATA[2] FSDATA[1] FSDATA[0] — RM703 [RMX4A] 33(J) — B12 A12 B13 A13
FSDATA[31] FSDATA[30] FSDATA[29] FSDATA[28] — RM704 [RMX4A] 33(J) — B15 A15 B16 A16
FSDATA[27] FSDATA[26] FSDATA[25] FSDATA[24] — RM705 [RMX4A] 33(J) — B18 A18 B19 A19
FSDATA[23] FSDATA[22] FSDATA[21] FSDATA[20] — RM706 [RMX4A] 33(J) — B20 A20 B21 A21
FSDATA[19] FSDATA[18] FSDATA[17] FSDATA[16] — RM737 [RMX4A] 33(J) — B23 A23 B24 A24

FSDATA0		FSBKSEL1	C20 → R736 → FSBKSEL[1]
FSDATA1		FSBKSEL0	C21 → R735 → FSBKSEL[0]
FSDATA2		FSCKE	C4 → R734 → FSCKE
FSDATA3		FSRAS	C24 → RM707 [RMX4A] 33(J) → /FSRAS
FSDATA4		FSCAS	D24 → RM707 → /FSCAS
FSDATA5		FSWE	C23 → RM707 → /FSWE
FSDATA6			
FSDATA7		FSDQM3	B22 → R740 → FSDQM[2]
FSDATA8		FSDQM2	B17 → R739 → FSDQM[3]
FSDATA9		FSDQM1	A11 → R738 → FSDQM[0]
FSDATA10		FSDQM0	A6 → R737 → FSDQM[1]
FSDATA11		FSDQS3	A22 → R744 → FSDQS[2]
FSDATA12		FSDQS2	A17 → R743 → FSDQS[3]
FSDATA13		FSDQS1	B11 → R742 → FSDQS[0]
FSDATA14		FSDQS0	B6 → R741 → FSDQS[1]
FSDATA15		FSCS1	C22 → R716 → /FSCS[1]
FSDATA16		FSCS0	D21 → R717 → /FSCS[0]
FSDATA17		FSCLKn	C5 → R718 → FSCLK-
FSDATA18		FSCLKp	D5 → R719 → FSCLK+
FSDATA19			
FSDATA20		FSADDR12	C11 → RM708 [RMX4A] 33(J) → FSADDR[11]
FSDATA21		FSADDR11	C14 → RM708 → FSADDR[4]
FSDATA22		FSADDR10	D16 → RM708 → FSADDR[9]
FSDATA23		FSADDR9	C12 → R707 33(J) → FSADDR[12] / FSADDR[10]
FSDATA24		FSADDR8	C6 → RM709 [RMX4A] 33(J) → FSADDR[8]
FSDATA25		FSADDR7	C7 → RM709 → FSADDR[7]
FSDATA26		FSADDR6	C8 → RM709 → FSADDR[6]
FSDATA27		FSADDR5	C10 → RM709 → FSADDR[5]
FSDATA28		FSADDR4	C13 → RM709 →
FSDATA29		FSADDR3	C15 → RM710 [RMX4A] 33(J) → FSADDR[3]
FSDATA30		FSADDR2	C16 → RM710 → FSADDR[2]
FSDATA31		FSADDR1	C17 → RM710 → FSADDR[1]
		FSADDR0	C18 → RM710 → FSADDR[0]

42DPC85
SIGNAL
DDR I/F

SH. NO.
702

/FSCAS. /FSCS[0]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDQM[0-1]. FSDQS[0-1]

/FSCAS. /FSCS[0]. /FSRAS. /FSWE. FS. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-15]. FSDQM[2-3]. FSDQS[2-3]

FSDATA[16-31]

HY5DU281622ETP-5 [MDSD8M16] IC701

HY5DU281622ETP-5 [MDSD8M16] IC702

FSADDR[0] 29 A0
FSADDR[1] 30 A1
FSADDR[2] 31 A2
FSADDR[3] 32 A3
FSADDR[4] 35 A4
FSADDR[5] 36 A5
FSADDR[6] 37 A6
FSADDR[7] 38 A7
FSADDR[8] 39 A8
FSADDR[9] 40 A9
FSADDR[10] 28 A10/AP
FSADDR[11] 41 A11

FSBKSEL[1] 27 BS1
FSBKSEL[0] 26 BS0
FSCLK- 46 CLK
FSCLK+ 45 CLK
FSCKE 44 CKE

R732 220(J) NOT MOUNT
R733 220(J) NOT MOUNT

/FSCS[0] 24 CS
/FSRAS 23 RAS
/FSCAS 22 CAS
/FSWE 21 WE

R720 33(J) FSDQM[0] 20 LDM
R721 33(J) FSDQM[1] 47 UDM
R722 33(J) FSDQS[0] 16 LDQS
R723 33(J) FSDQS[1] 51 UDQS

R724 33(J) FSDQM[2] 20 LDM
R725 33(J) FSDQM[3] 47 UDM
R726 33(J) FSDQS[2] 16 LDQS
R727 33(J) FSDQS[3] 51 UDQS

14 NC
17 NC
19 NC
25 NC
FSADDR[12] 42 NC
43 NC
50 NC
53 NC

DQ0 DQ1 DQ2 DQ3 DQ4 DQ5 DQ6 DQ7 DQ8 DQ9 DQ10 DQ11 DQ12 DQ13 DQ14 DQ15

[RMX4A] RM711 33(J): 2 1 / 4 3 / 5 7 → FSDATA[0] FSDATA[1] FSDATA[2] FSDATA[3]
[RMX4A] RM712 33(J): 8 10 11 13 → FSDATA[4] FSDATA[5] FSDATA[6] FSDATA[7]
[RMX4A] RM713 33(J): 54 56 57 59 → FSDATA[8] FSDATA[9] FSDATA[10] FSDATA[11]
[RMX4A] RM714 33(J): 60 62 63 65 → FSDATA[12] FSDATA[13] FSDATA[14] FSDATA[15]

[RMX4A] RM715 33(J): 2 1 / 4 3 / 5 7 → FSDATA[16] FSDATA[17] FSDATA[18] FSDATA[19]
[RMX4A] RM716 33(J): 8 10 11 13 → FSDATA[20] FSDATA[21] FSDATA[22] FSDATA[23]
[RMX4A] RM717 33(J): 54 56 57 59 → FSDATA[24] FSDATA[25] FSDATA[26] FSDATA[27]
[RMX4A] RM718 33(J): 60 62 63 65 → FSDATA[28] FSDATA[29] FSDATA[30] FSDATA[31]

[VDDQ] 3 9 15 55 61
[VSSQ] 6 12 52 58 64
[VDD] 1 18
[VSS] 34 48
33
[VREF] 49
66

+2.5V_DDR
FSVREF

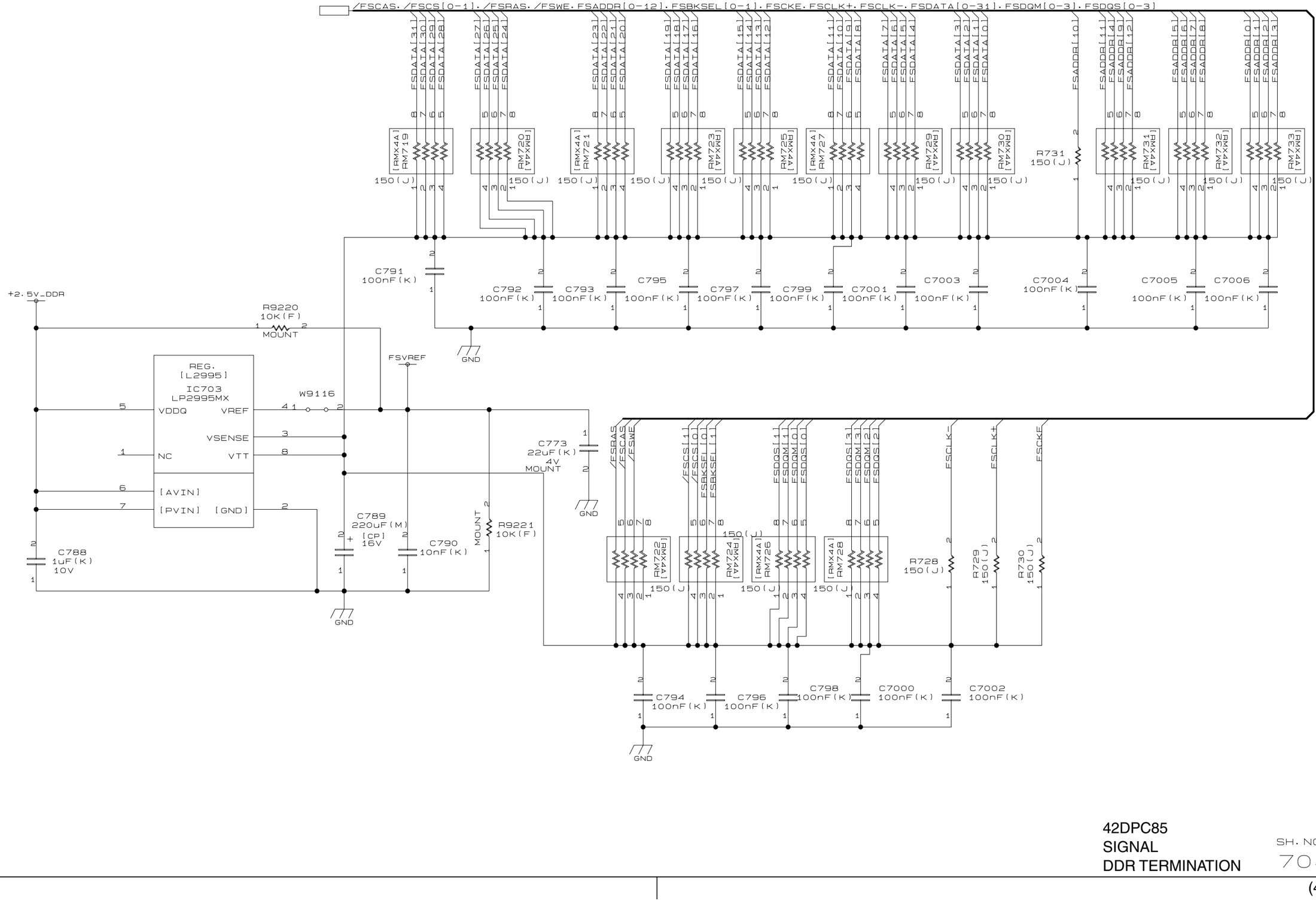
C774 100nF(K)
C775 100nF(K)
C776 100nF(K)
C777 100nF(K)
C778 100nF(K)
C779 100nF(K)
C780 100nF(K)

C781 100nF(K)
C782 100nF(K)
C783 100nF(K)
C784 100nF(K)
C785 100nF(K)
C786 100nF(K)
C787 100nF(K)

GND

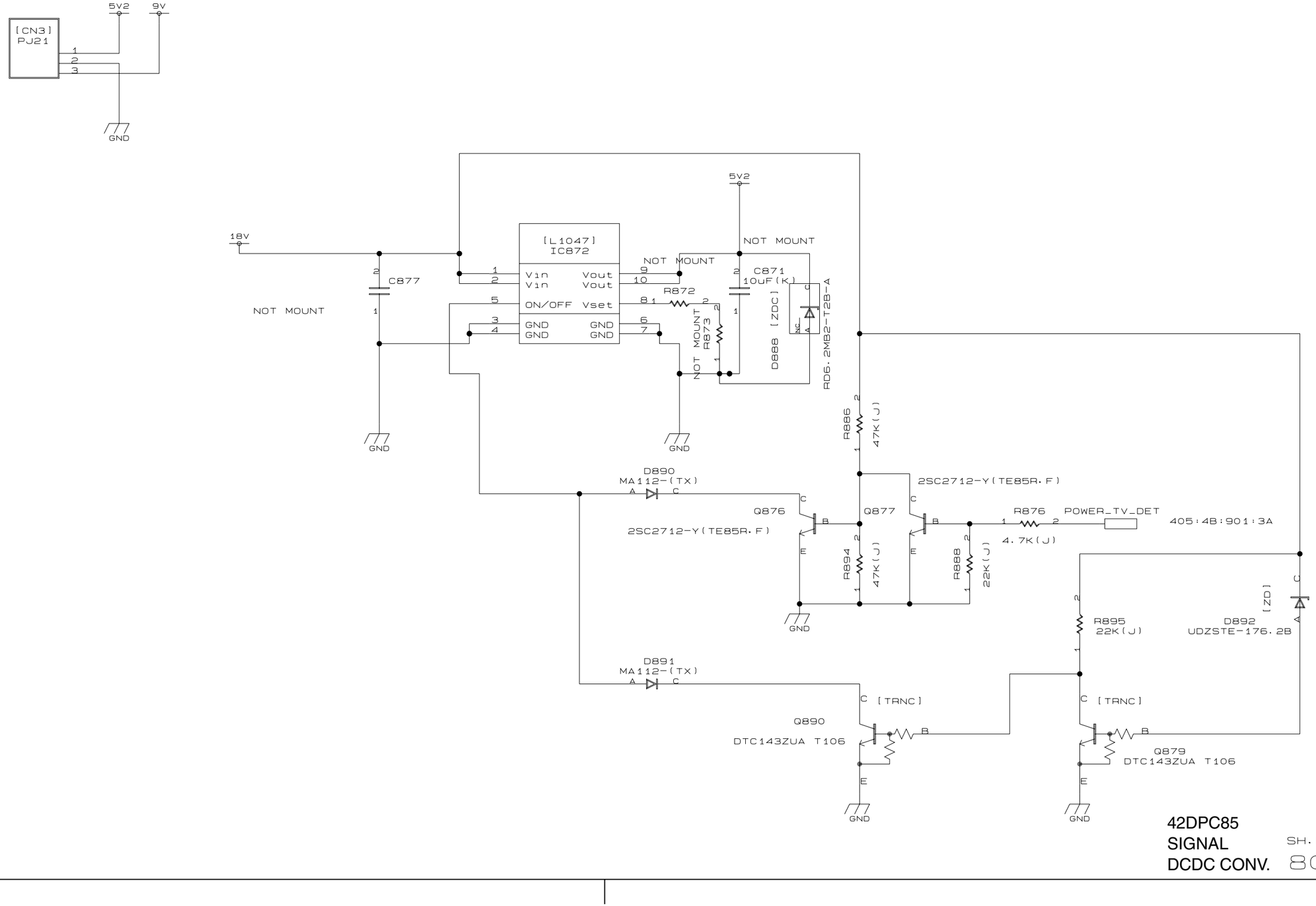
42DPC85
SIGNAL
DDR SDRAM

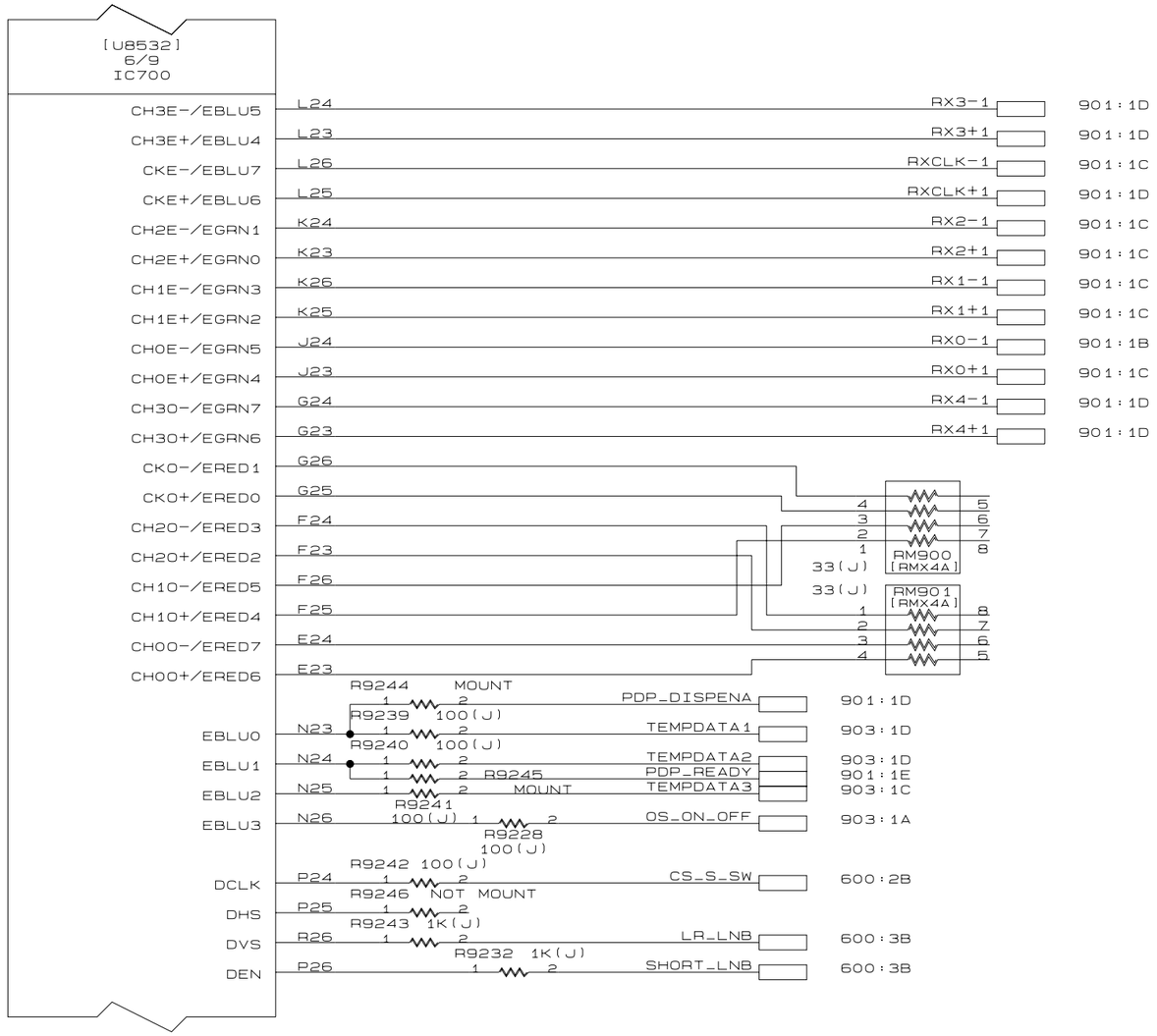
SH. NO. 703



42DPC85
SIGNAL
DDR TERMINATION

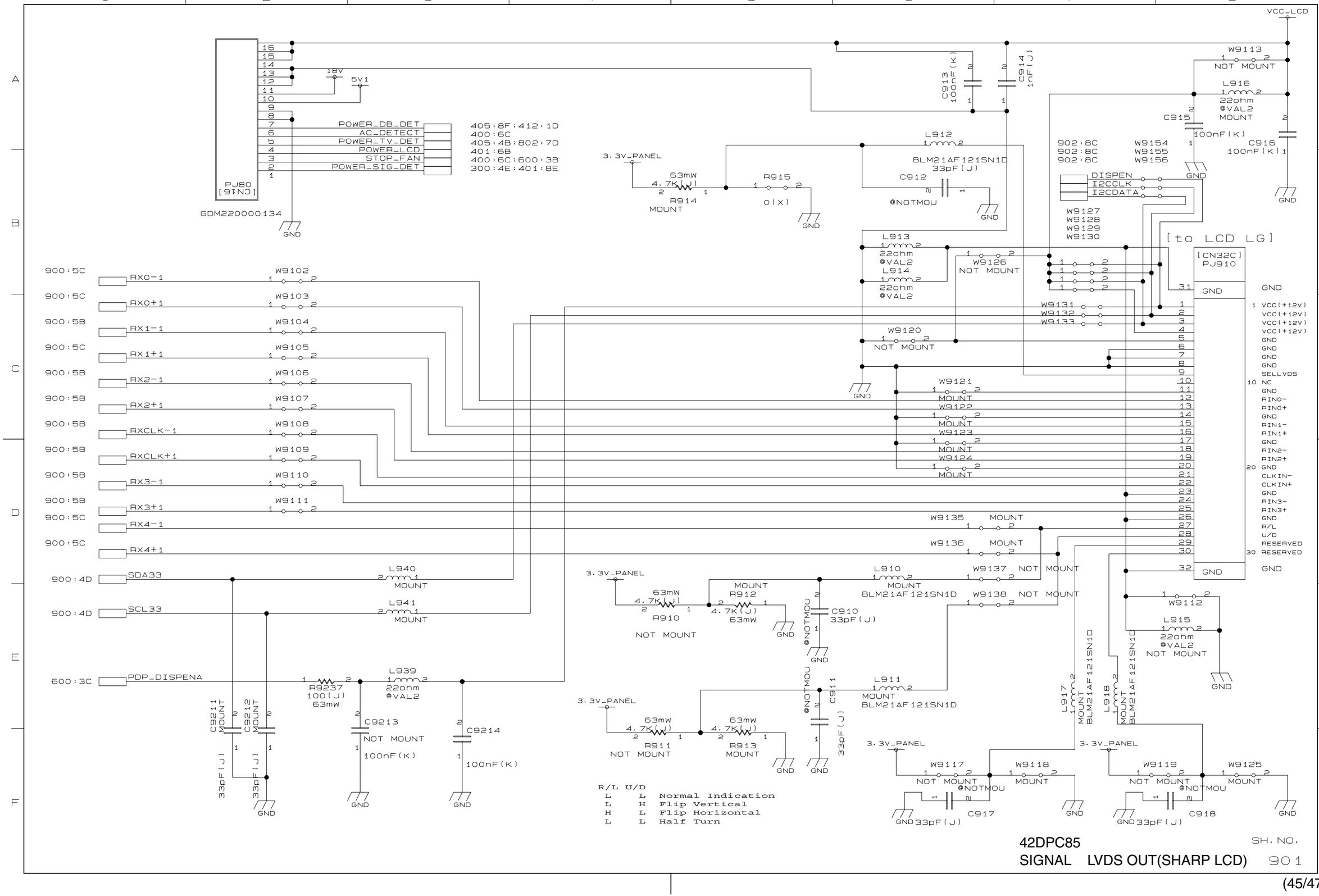
SH. NO.
704

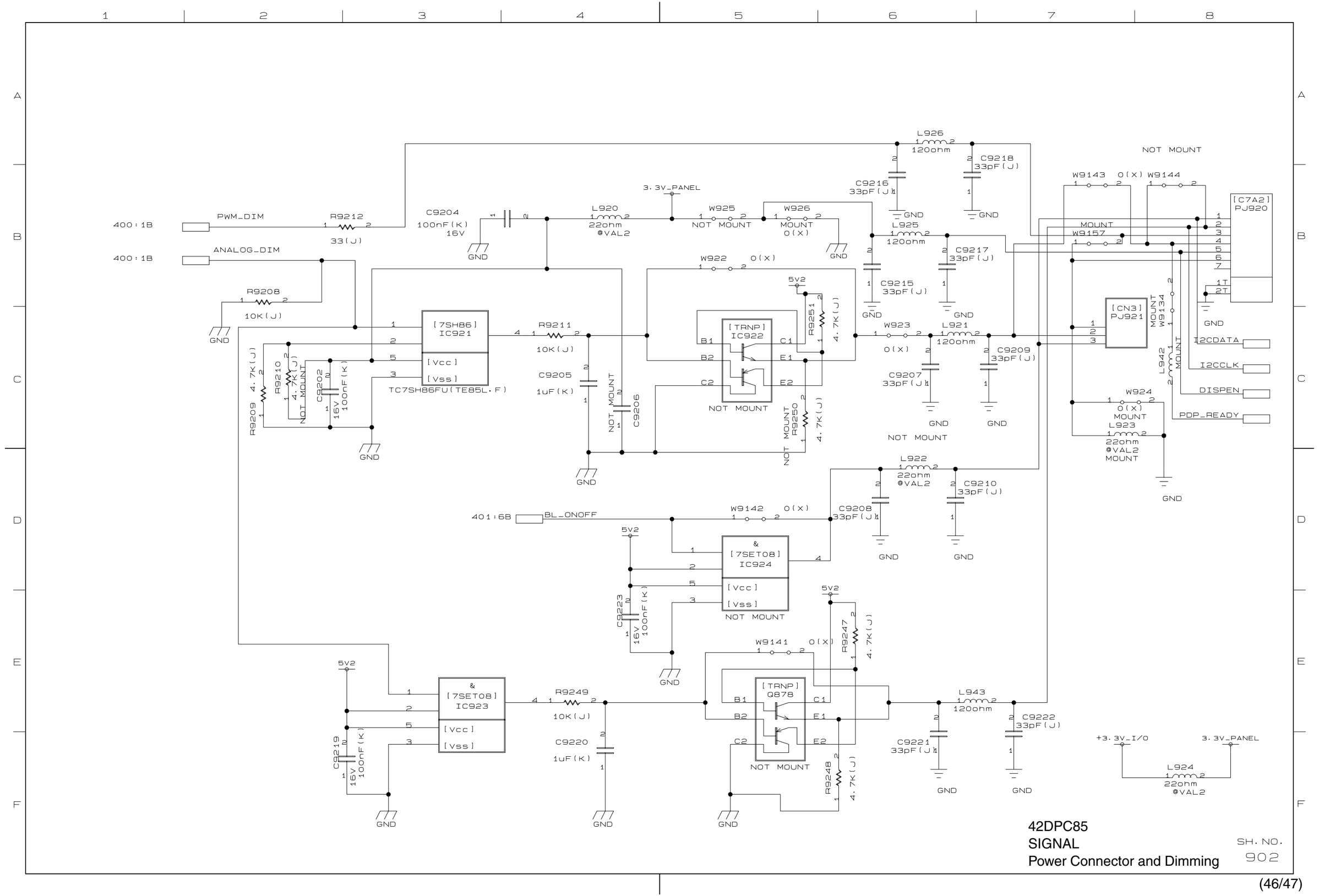

[CN3]
PJ21
1
2
3
5V2
9V
GND
18V
C877
NOT MOUNT
[L1047]
IC872
Vin
Vin
ON/OFF
GND
GND
Vout
Vout
Vset
GND
GND
NOT MOUNT
R872
NOT MOUNT
R873
5V2
NOT MOUNT
C871
10uF(K)
D888 [ZDC]
RD6.2MB2-T2B-A
R886
47K(J)
D890
MA112-(TX)
Q876
2SC2712-Y(TE85R.F)
Q877
2SC2712-Y(TE85R.F)
R876
4.7K(J)
POWER_TV_DET
405:4B:901:3A
R894
47K(J)
R888
22K(J)
R895
22K(J)
D892
UDZSTE-176.2B
[ZD]
D891
MA112-(TX)
Q890
DTC143ZUA T106
[TRNC]
Q879
DTC143ZUA T106
[TRNC]
42DPC85
SIGNAL
DCDC CONV.
SH. NO.
802

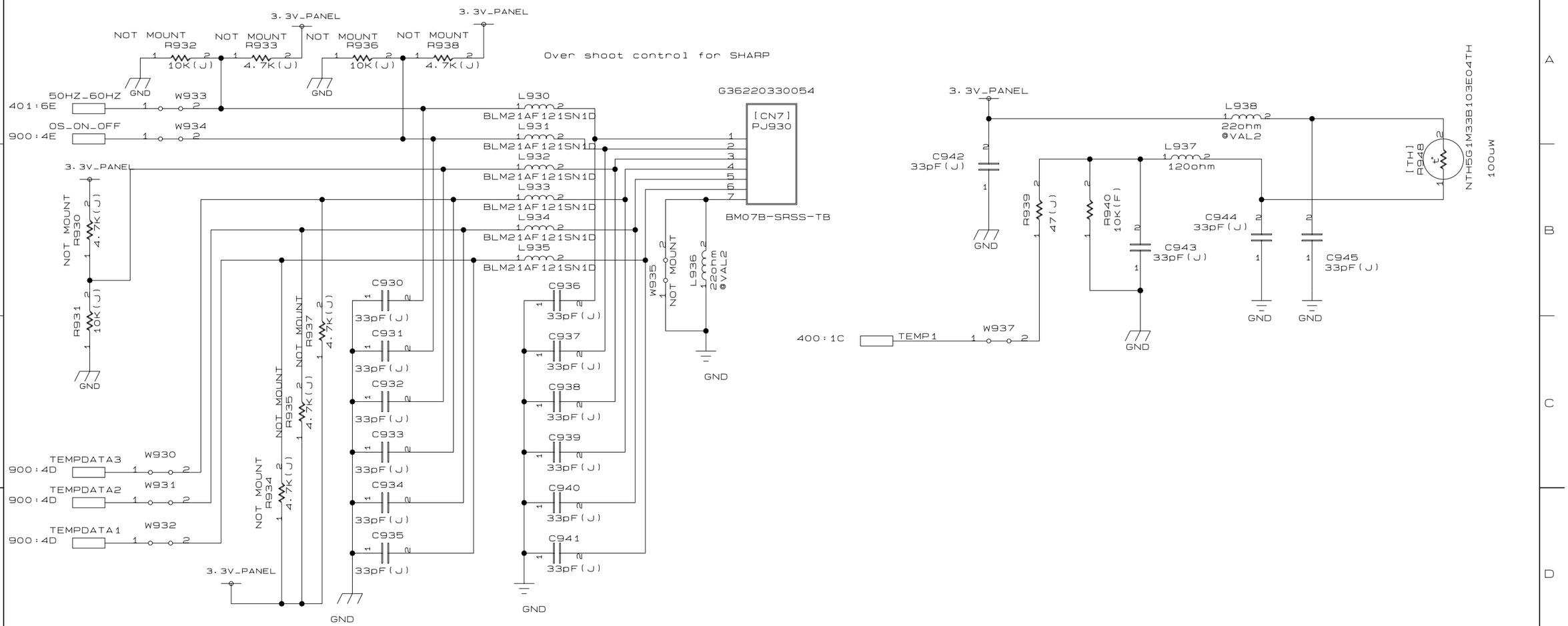


42DPC85
SIGNAL
LVDS OUT

SH. NO.
900


PJ80 [CN16]
GDM2200000134
18V
5V1
POWER_DB_DET
AC_DETECT
POWER_TV_DET
POWER_LCD
STOP_FAN
POWER_SIG_DET
405:8F:412:1D
400:6C
405:4B:802:7D
401:6B
400:6C:600:3B
300:4E:401:8E
3.3V_PANEL
63mW
4.7K(J)
R914
MOUNT
R915
0(X)
C913 100nF(K)
C914 1nF(J)
L912
BLM21AF121SN1D
33pF(J)
C912
@NOTMOU
W9113
NOT MOUNT
L916
22ohm
@VAL2
MOUNT
C915
100nF(K)
C916
100nF(K)
902:8C
902:8C
902:8C
W9154
W9155
W9156
DISPEN
I2CCLK
I2CDATA
W9127
W9128
W9129
W9130
[to LCD LG]
[CN32C]
PJ910
L913
22ohm
@VAL2
L914
22ohm
@VAL2
W9126
NOT MOUNT
W9131
W9132
W9133
W9120
NOT MOUNT
W9121
MOUNT
W9122
MOUNT
W9123
MOUNT
W9124
MOUNT
W9135 MOUNT
W9136 MOUNT
W9137 NOT MOUNT
W9138 NOT MOUNT
W9112
L915
22ohm
@VAL2
NOT MOUNT
L917
MOUNT
BLM21AF121SN1D
L918
MOUNT
BLM21AF121SN1D
900:5C RX0-1
900:5C RX0+1
900:5B RX1-1
900:5C RX1+1
900:5B RX2-1
900:5B RX2+1
900:5B RXCLK-1
900:5B RXCLK+1
900:5B RX3-1
900:5B RX3+1
900:5C RX4-1
900:5C RX4+1
W9102
W9103
W9104
W9105
W9106
W9107
W9108
W9109
W9110
W9111
900:4D SDA33
900:4D SCL33
L940
L941
MOUNT
600:3C PDP_DISPENA
R9237
100(J)
63mW
L939
22ohm
@VAL2
C9211 MOUNT 33pF(J)
C9212 MOUNT 33pF(J)
C9213 NOT MOUNT 100nF(K)
C9214 100nF(K)
3.3V_PANEL
R910 NOT MOUNT
R912 4.7K(J) 63mW
MOUNT
C910 33pF(J)
@NOTMOU
L910 MOUNT BLM21AF121SN1D
3.3V_PANEL
R911 NOT MOUNT
R913 MOUNT
C911 33pF(J)
@NOTMOU
L911 MOUNT BLM21AF121SN1D
3.3V_PANEL
W9117 NOT MOUNT
W9118 MOUNT
C917 33pF(J)
@NOTMOU
3.3V_PANEL
W9119 NOT MOUNT
W9125 MOUNT
C918 33pF(J)
@NOTMOU
R/L U/D
L L Normal Indication
L H Flip Vertical
H L Flip Horizontal
L L Half Turn
GND
VCC_LCD
1 VCC(+12V)
VCC(+12V)
VCC(+12V)
VCC(+12V)
GND
GND
GND
GND
SELLVDS
10 NC
GND
RIN0-
RIN0+
GND
RIN1-
RIN1+
GND
RIN2-
RIN2+
20 GND
CLKIN-
CLKIN+
GND
RIN3-
RIN3+
GND
R/L
U/D
RESERVED
30 RESERVED
GND
42DPC85
SIGNAL LVDS OUT(SHARP LCD)
SH. NO.
901


PWM_DIM
400:1B
R9212
33(J)
ANALOG_DIM
400:1B
R9208
10K(J)
GND
C9204
100nF(K)
16V
3.3V_PANEL
L920
22ohm
@VAL2
W925
NOT MOUNT
W926
MOUNT
0(X)
L926
120ohm
C9216
33pF(J)
C9218
33pF(J)
NOT MOUNT
W9143 0(X) W9144
[C7A2]
PJ920
L925
120ohm
C9217
33pF(J)
C9215
33pF(J)
MOUNT
W9157
W922 0(X)
5V2
R9251
4.7K(J)
[7SH86]
IC921
[Vcc]
[Vss]
TC7SH86FU(TE85L,F)
R9211
10K(J)
C9205
1uF(K)
NOT MOUNT
C9206
R9209 4.7K(J)
R9210
4.7K(J)
NOT MOUNT
C9202
16V
100nF(K)
[TRNP]
IC922
B1
B2
C2
C1
E1
E2
NOT MOUNT
R9250
4.7K(J)
NOT MOUNT
W923
0(X)
L921
120ohm
C9209
33pF(J)
C9207
33pF(J)
[CN3]
PJ921
MOUNT
W9134
L942
MOUNT
1T
2T
GND
I2CDATA
I2CCLK
DISPEN
PDP_READY
W924
0(X)
MOUNT
L923
22ohm
@VAL2
MOUNT
NOT MOUNT
L922
220hm
@VAL2
C9210
33pF(J)
C9208
33pF(J)
401:6B
BL_ONOFF
W9142 0(X)
&
[7SET08]
IC924
[Vcc]
[Vss]
NOT MOUNT
C9223
16V
100nF(K)
W9141 0(X)
R9247
4.7K(J)
5V2
&
[7SET08]
IC923
[Vcc]
[Vss]
C9219
16V
100nF(K)
R9249
10K(J)
C9220
1uF(K)
[TRNP]
Q878
B1
B2
C2
C1
E1
E2
NOT MOUNT
R9248
4.7K(J)
L943
120ohm
C9221
33pF(J)
C9222
33pF(J)
+3.3V_I/O
3.3V_PANEL
L924
22ohm
@VAL2
42DPC85
SIGNAL
Power Connector and Dimming
SH. NO.
902



42DPC85
SIGNAL
LVDS Power and Others

SH. NO. 903